

FIG. 1

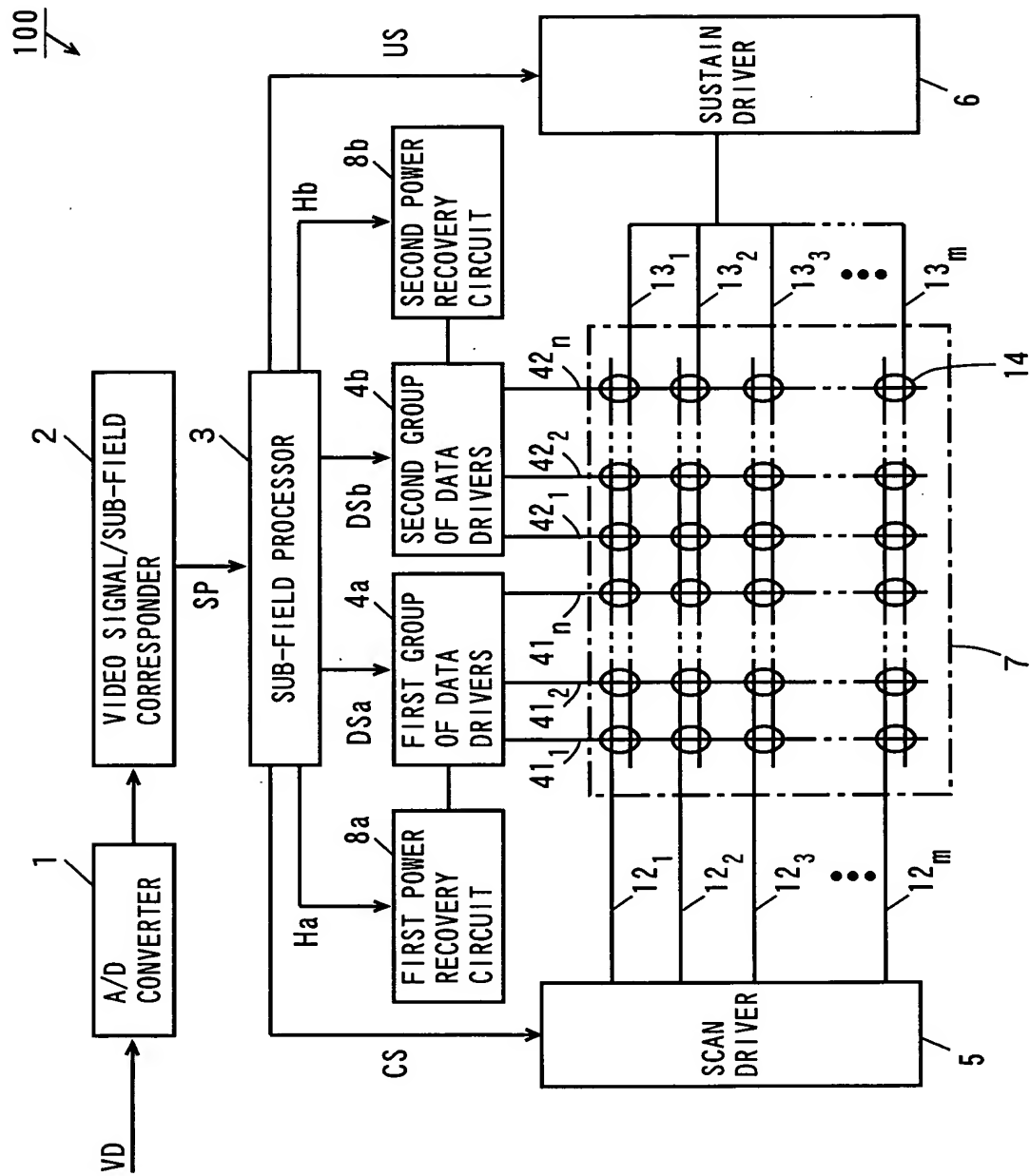


FIG. 2

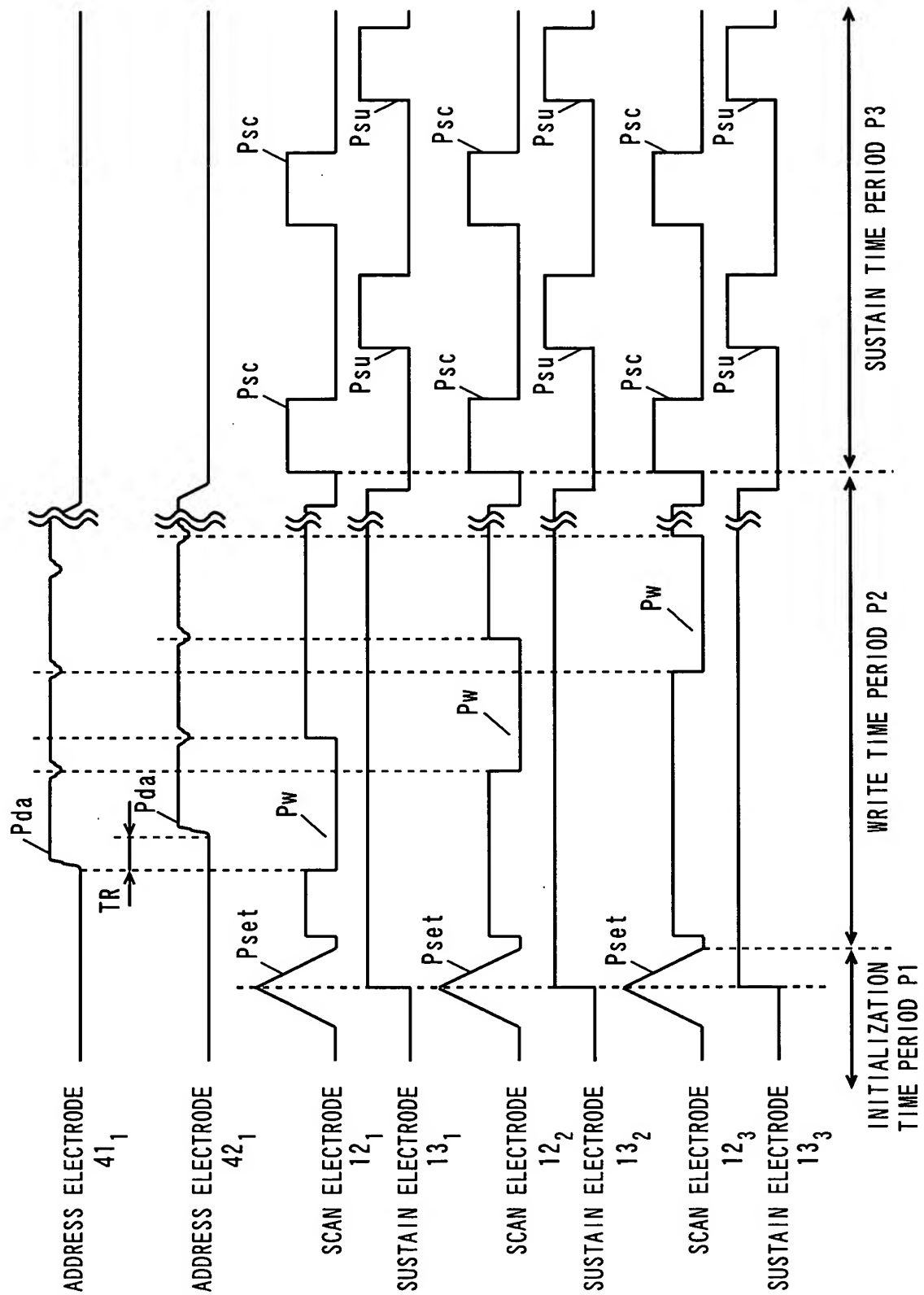


FIG. 3

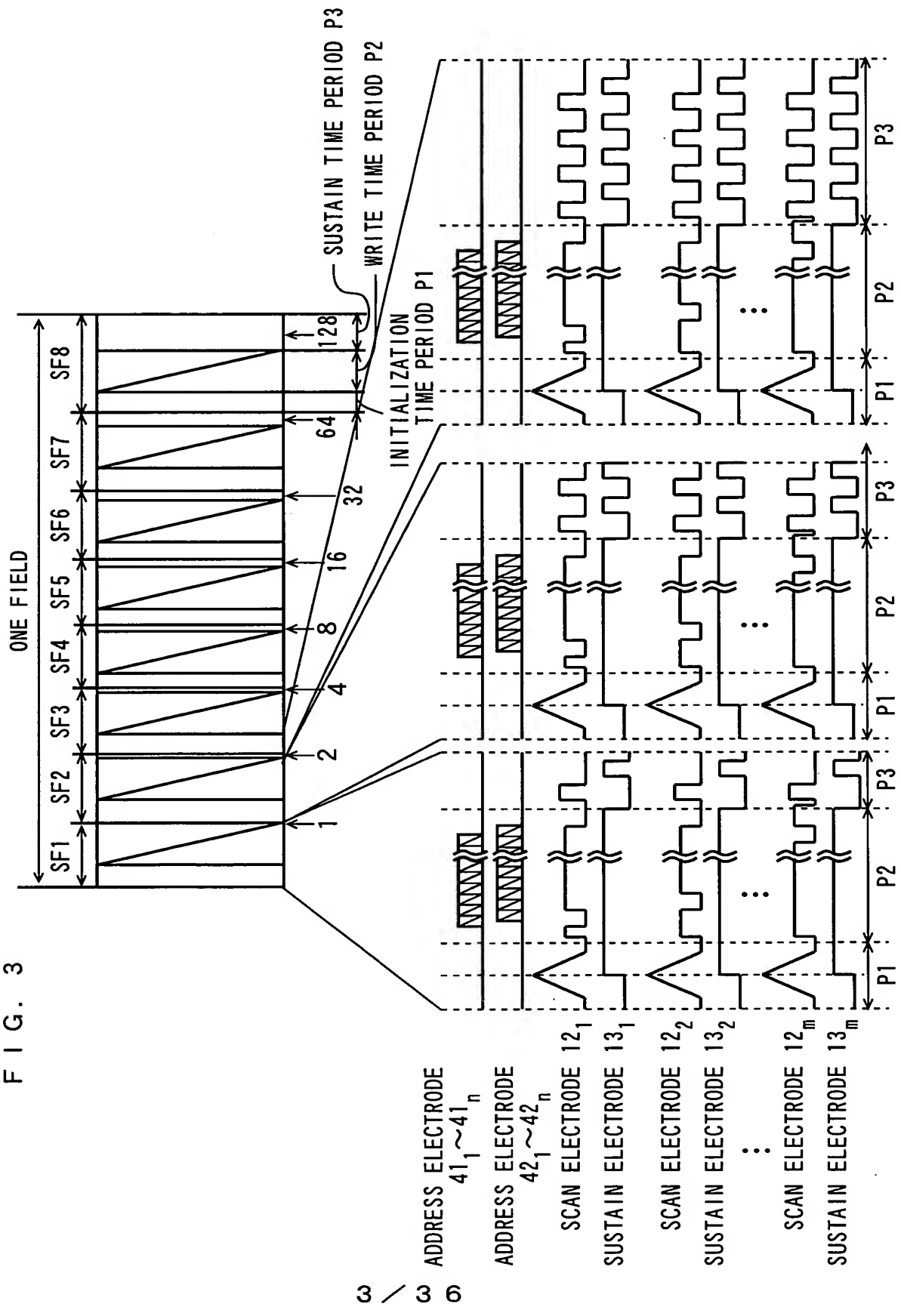


FIG. 4

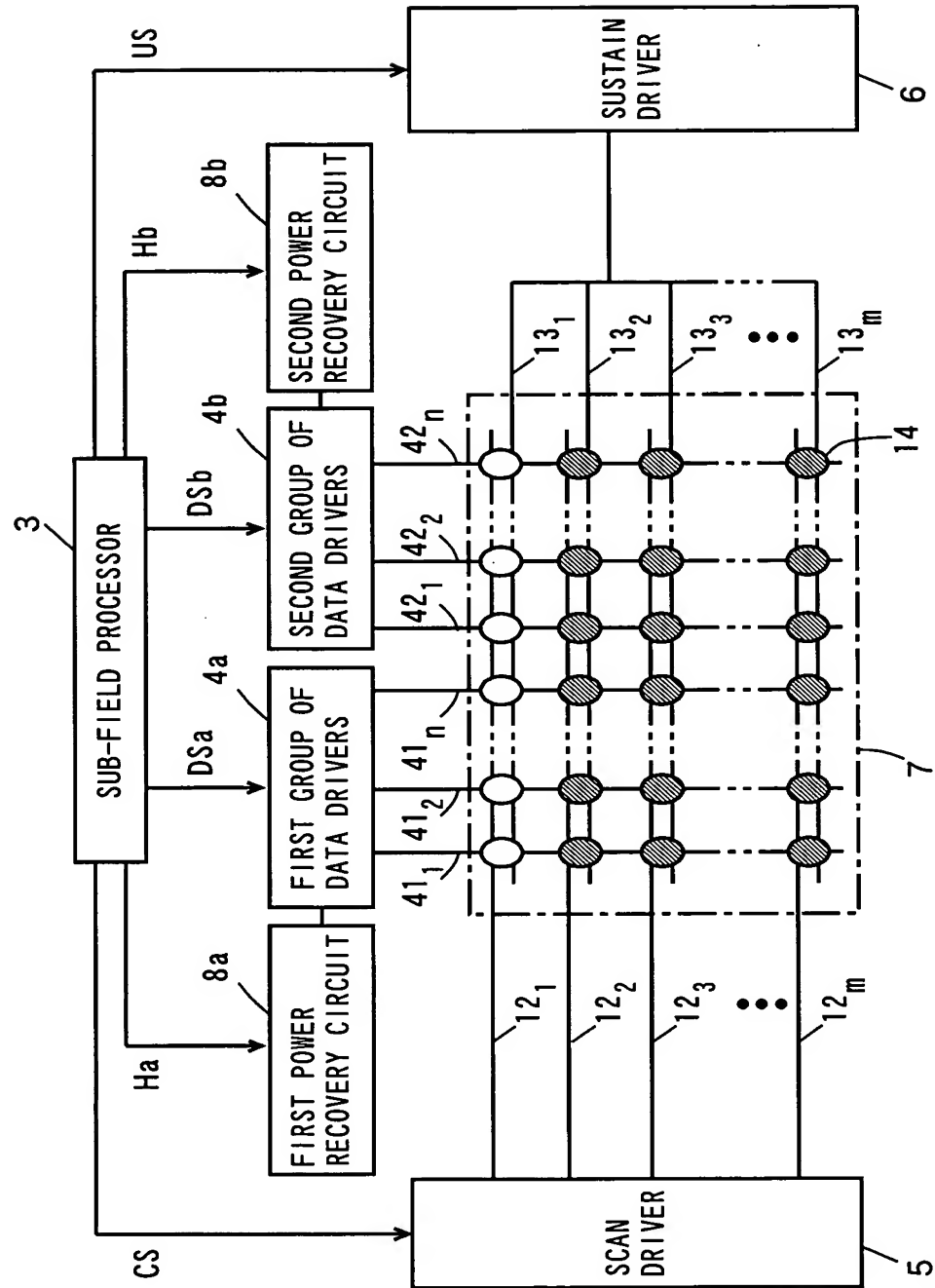


FIG. 5

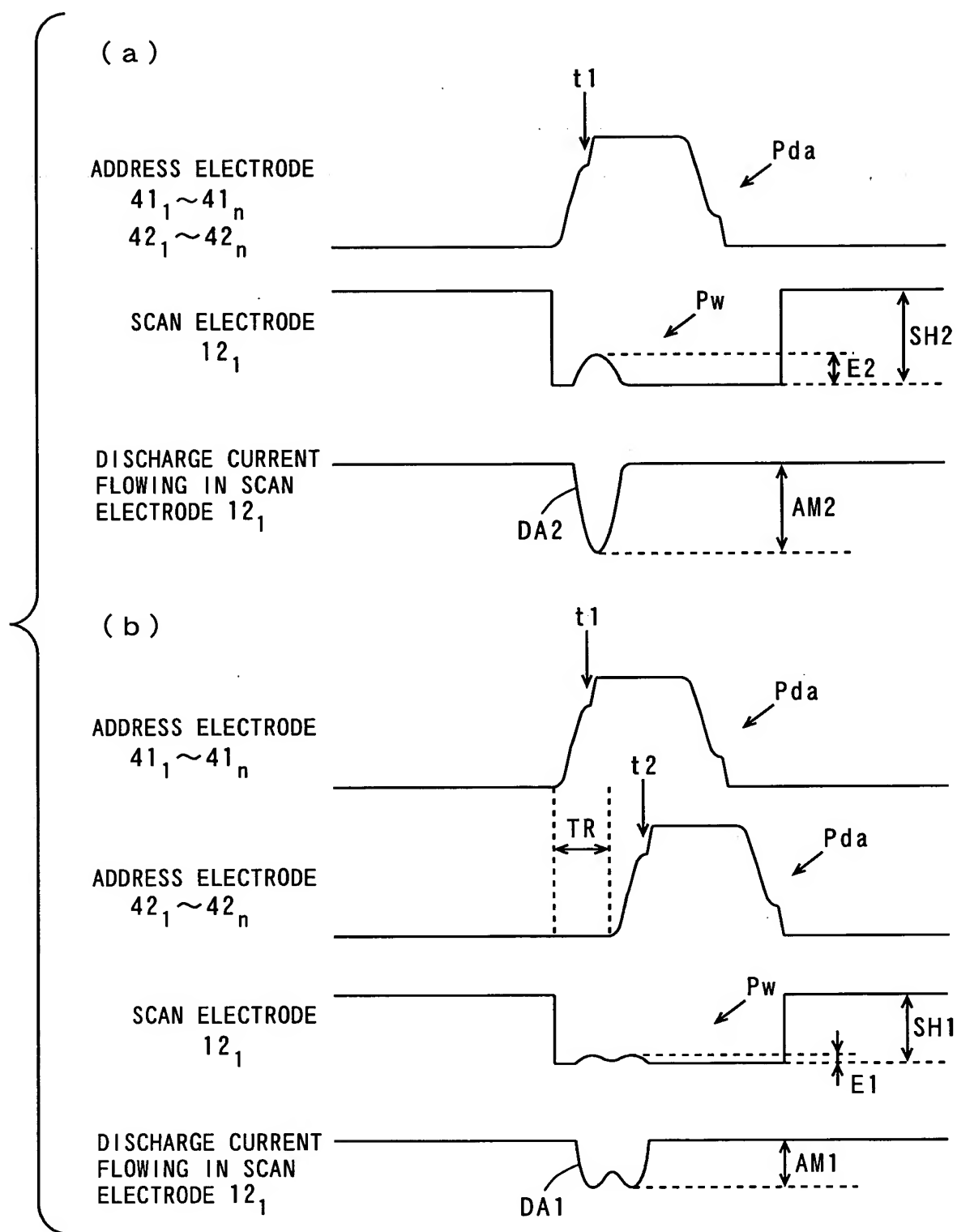


FIG. 6

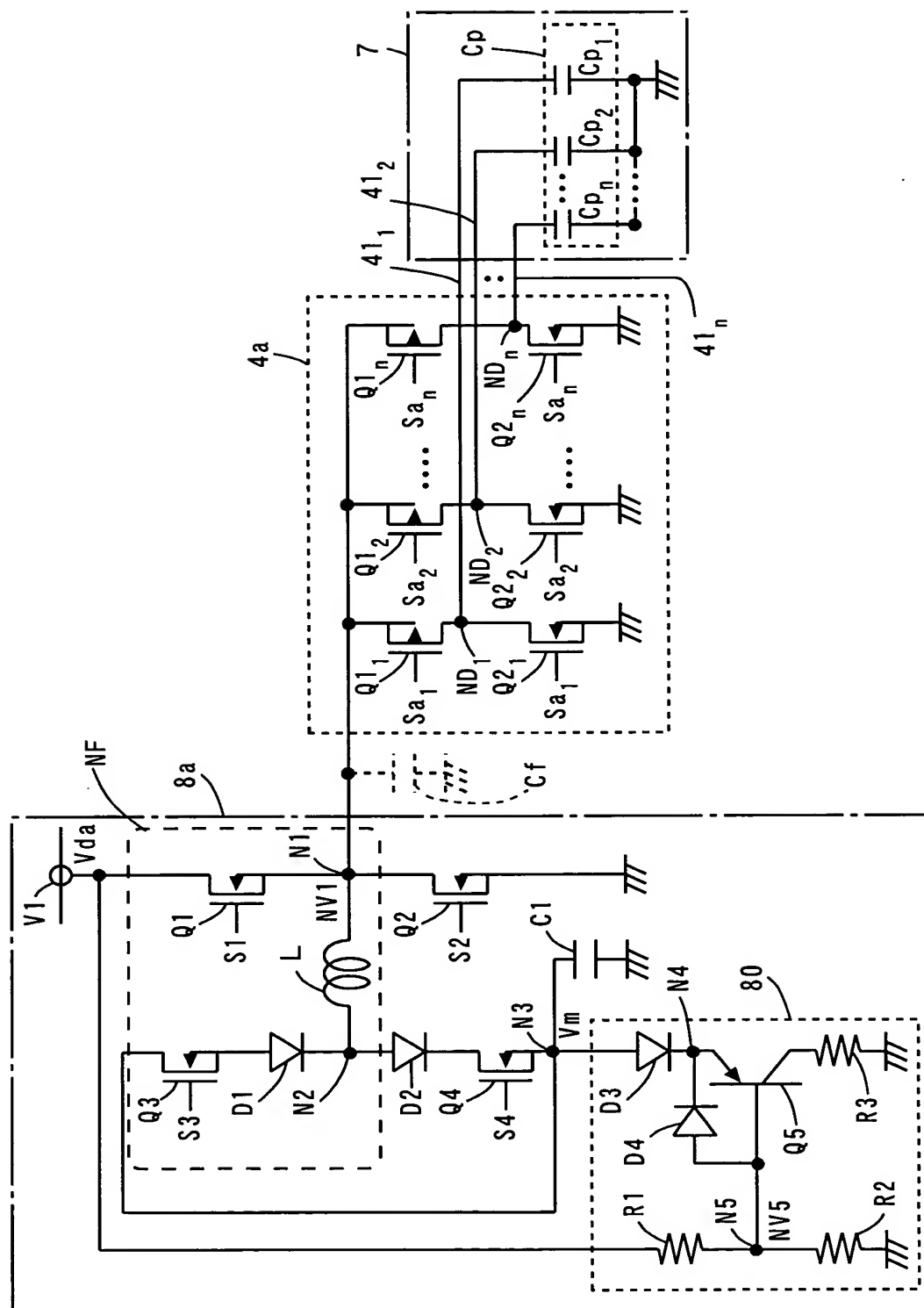


FIG. 7

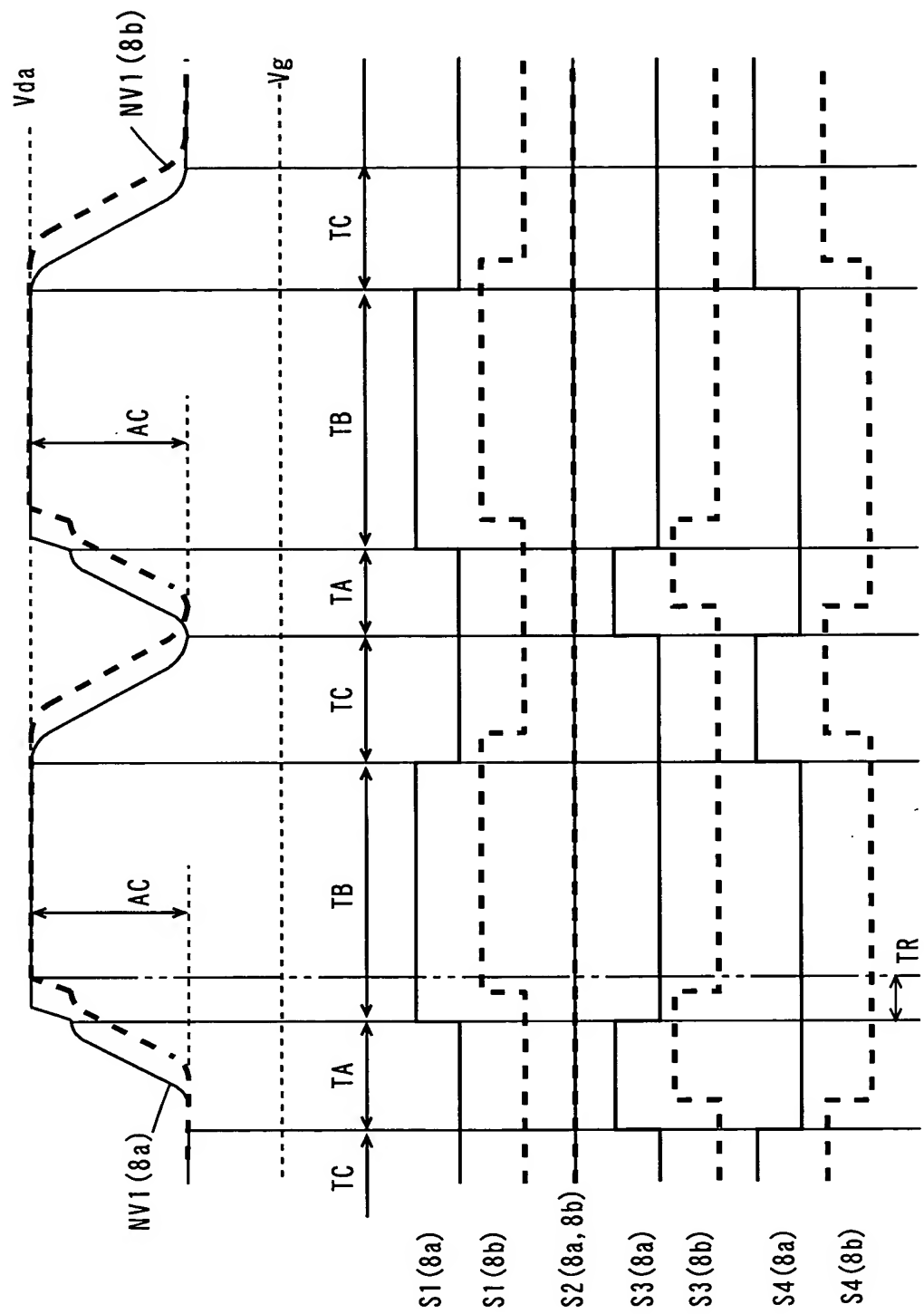


FIG. 8

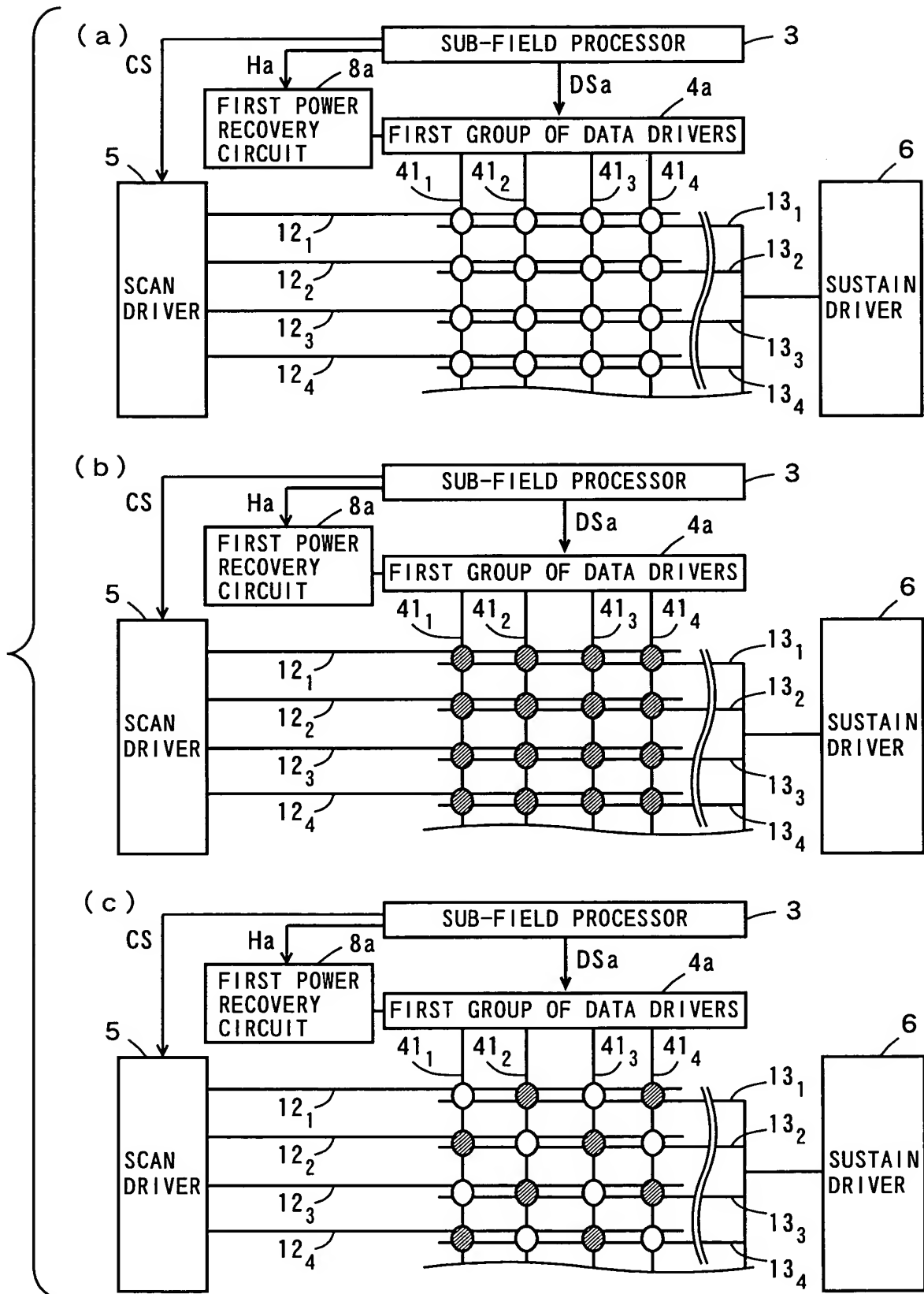


FIG. 9

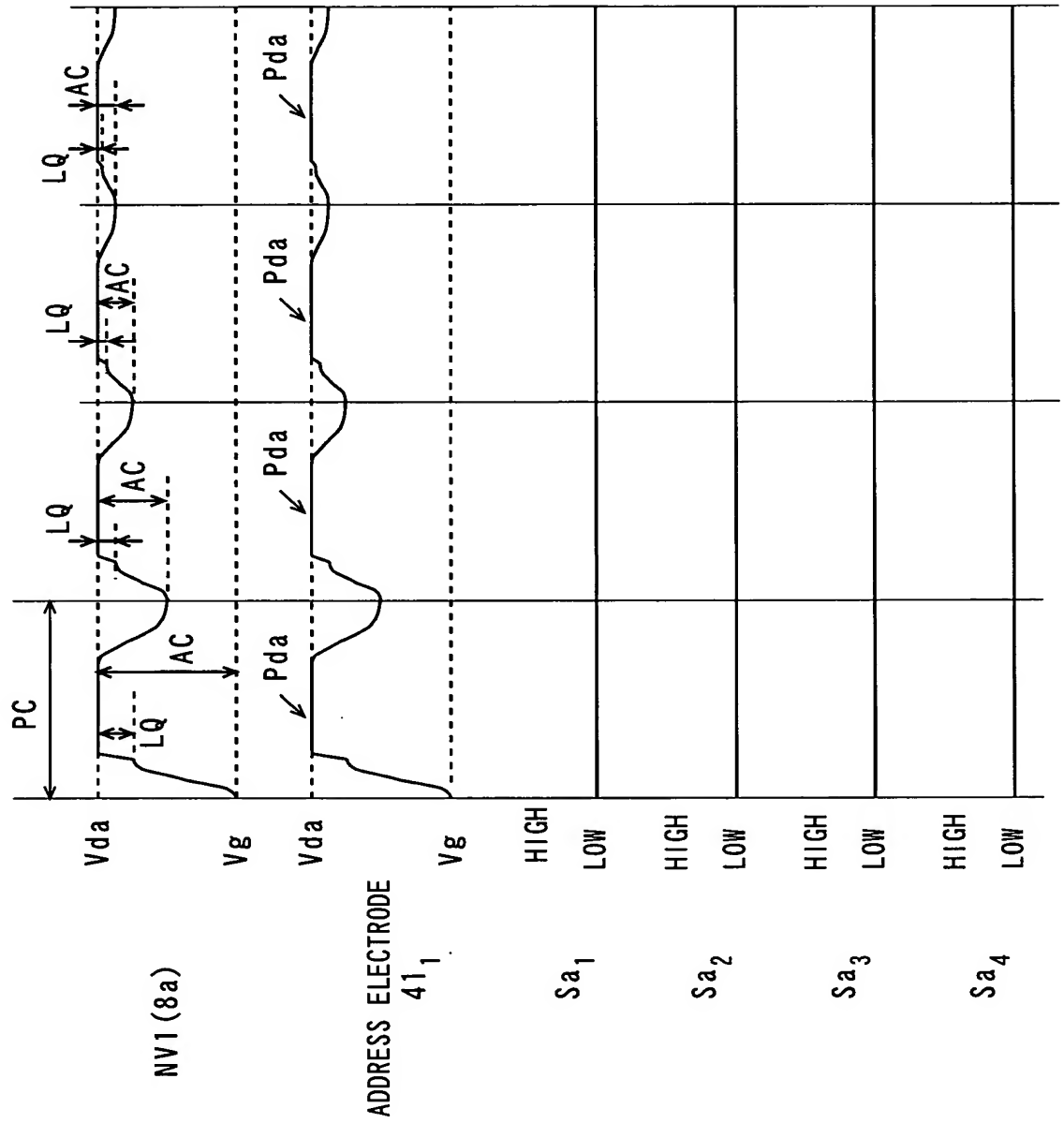


FIG. 10

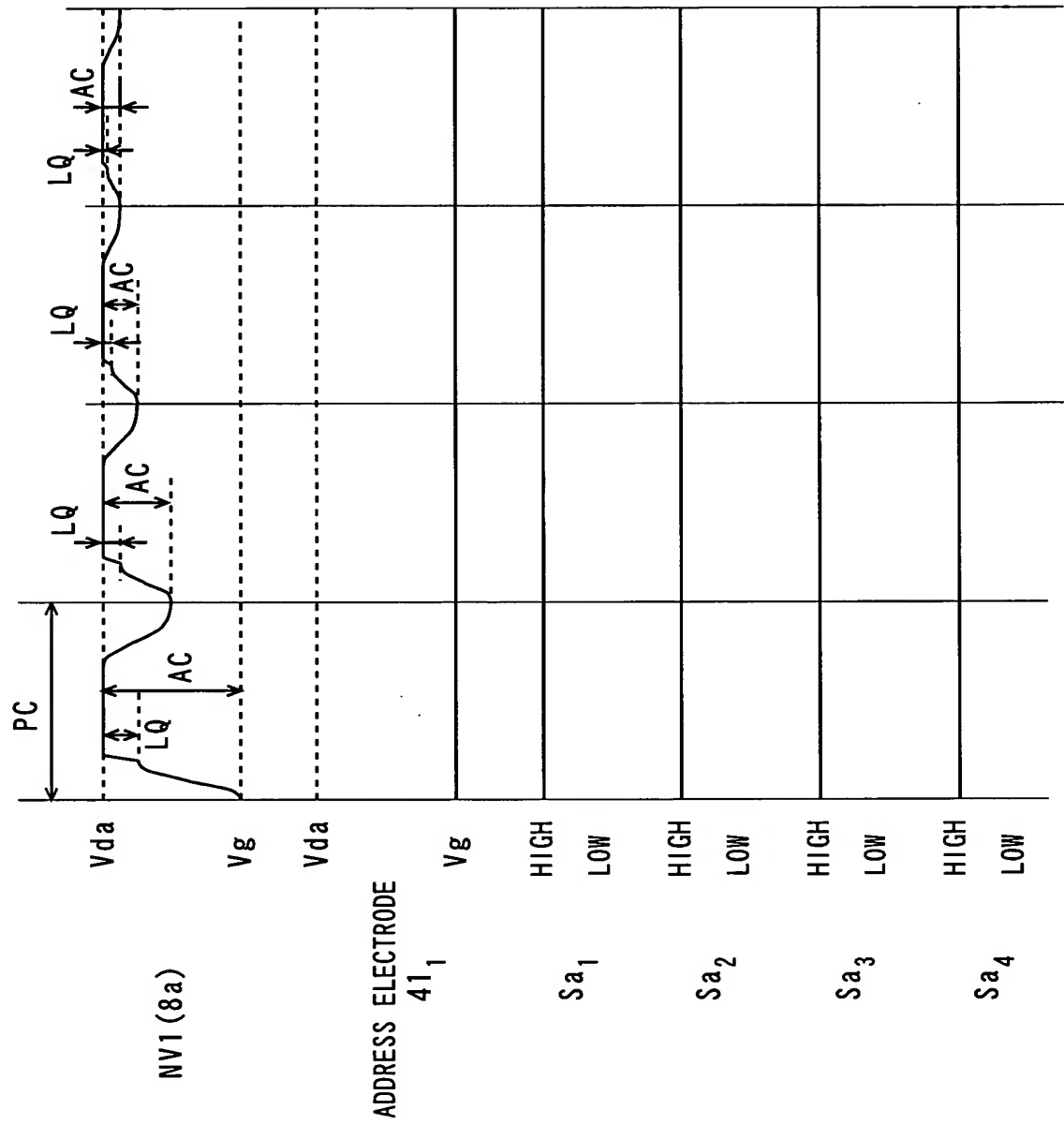
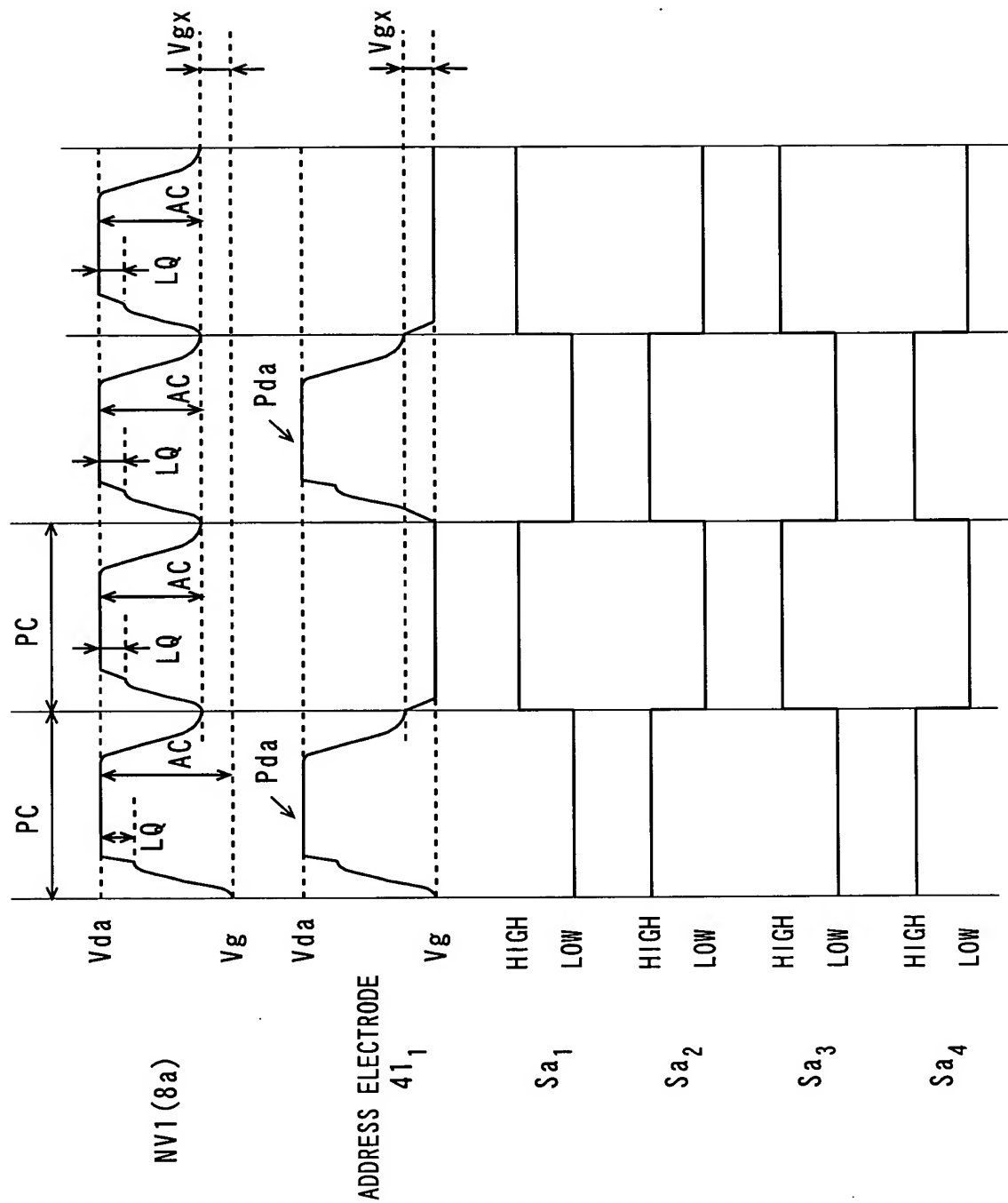


FIG. 11



F I G. 1 2

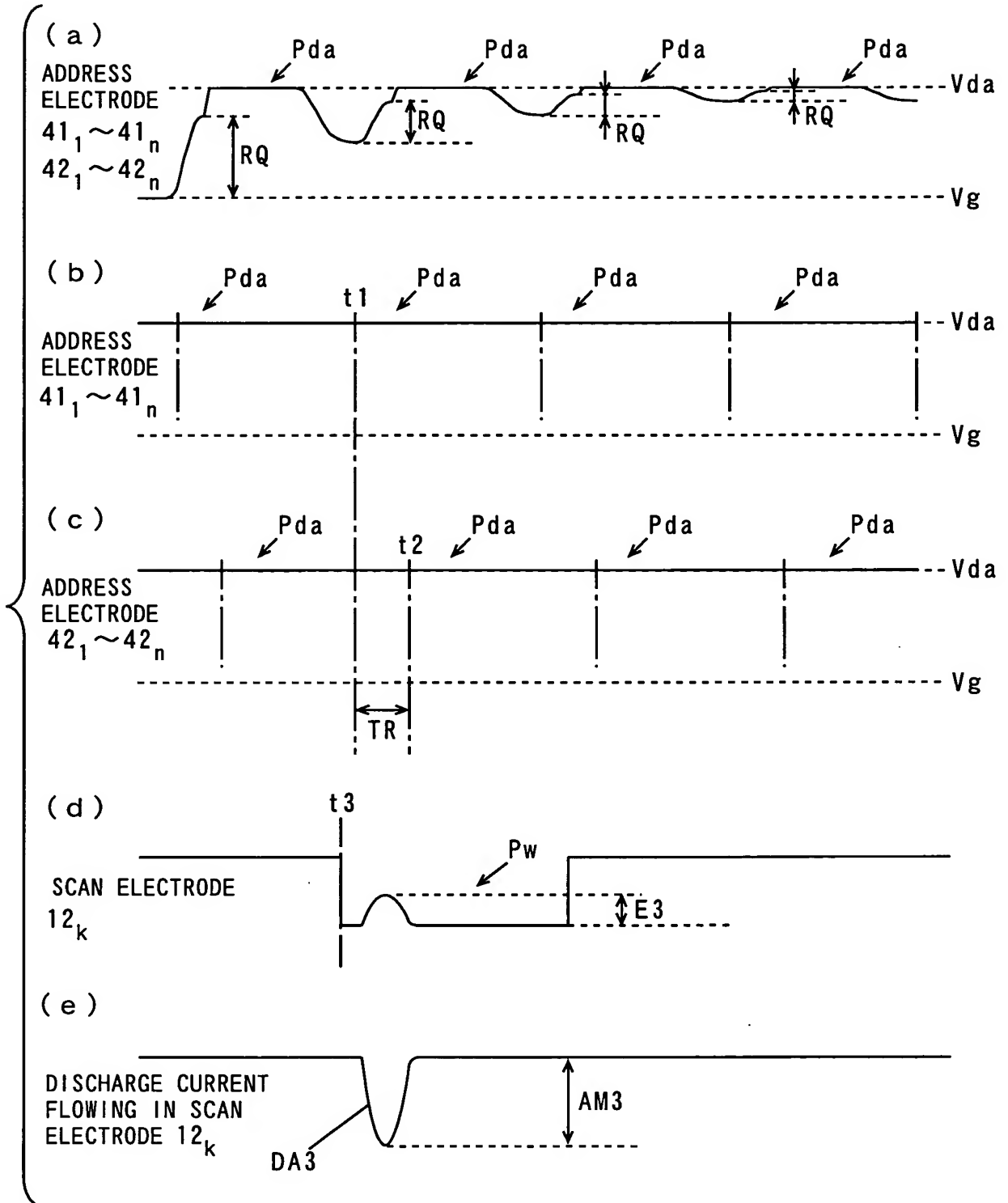


FIG. 13

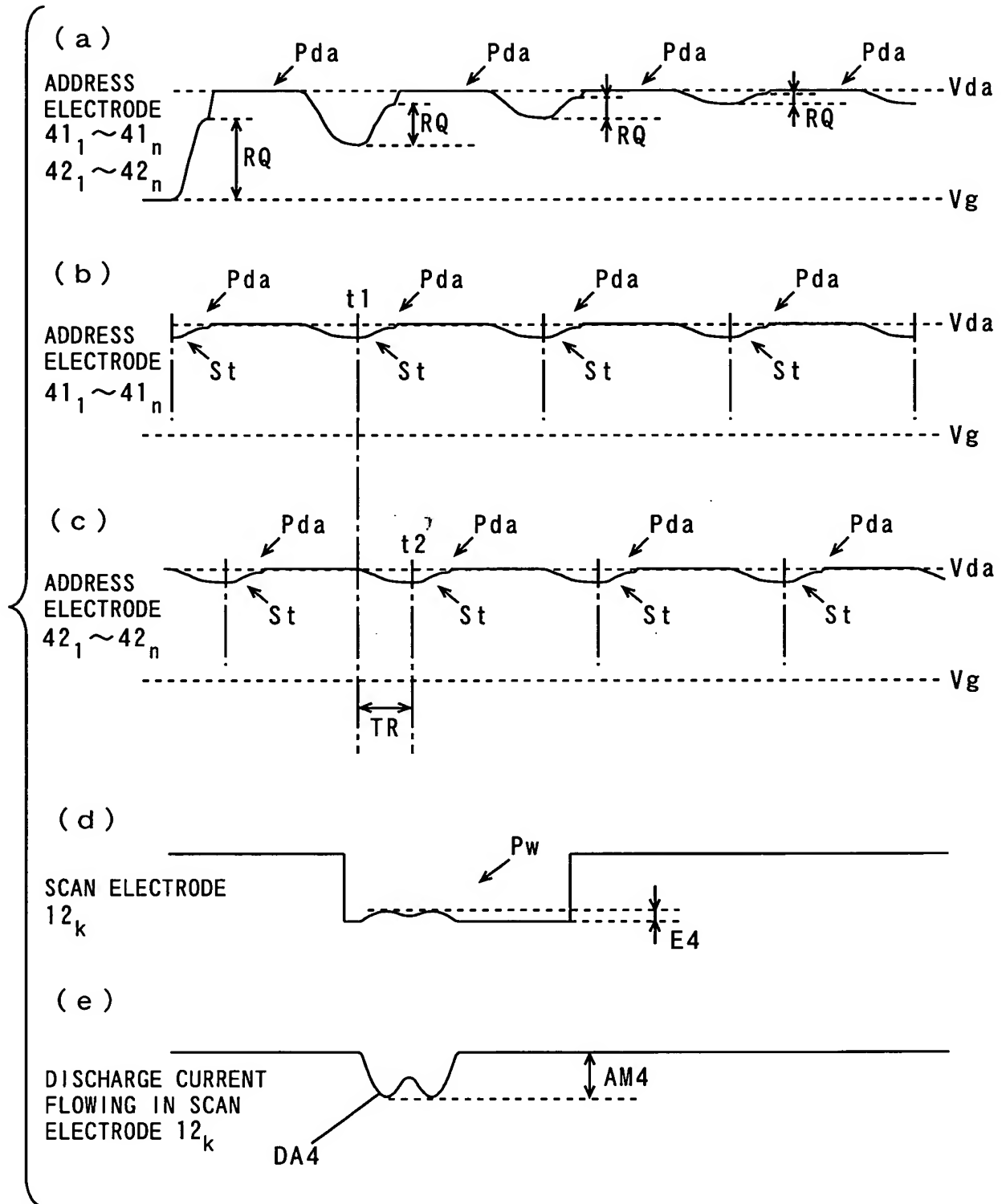


FIG. 14

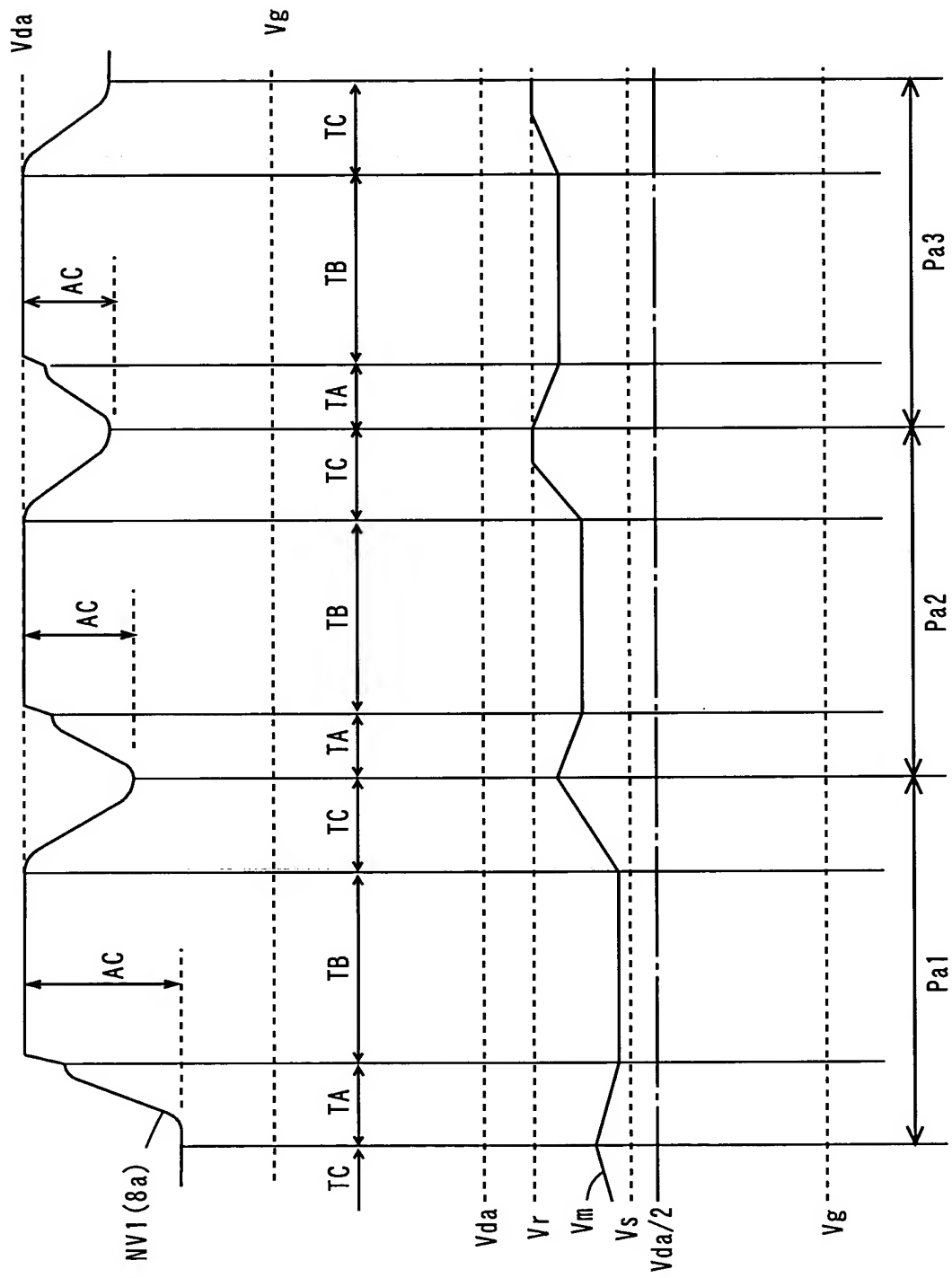


FIG. 15

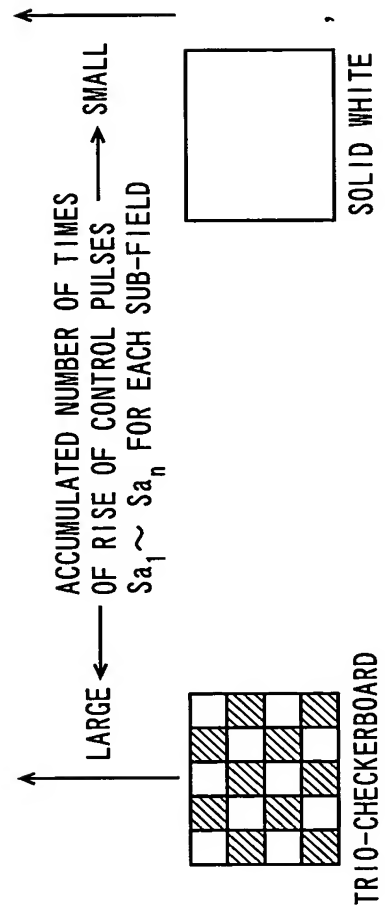
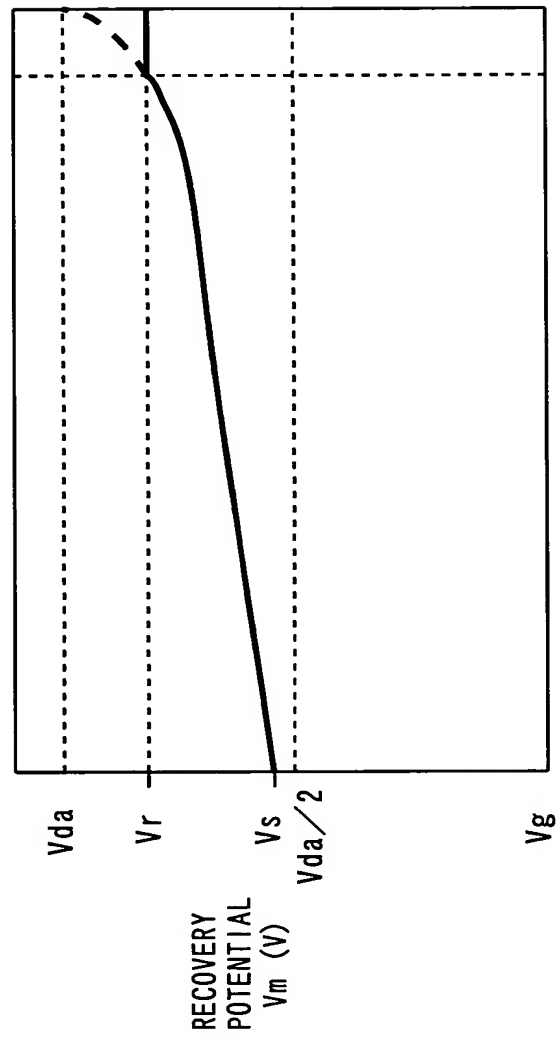


FIG. 16

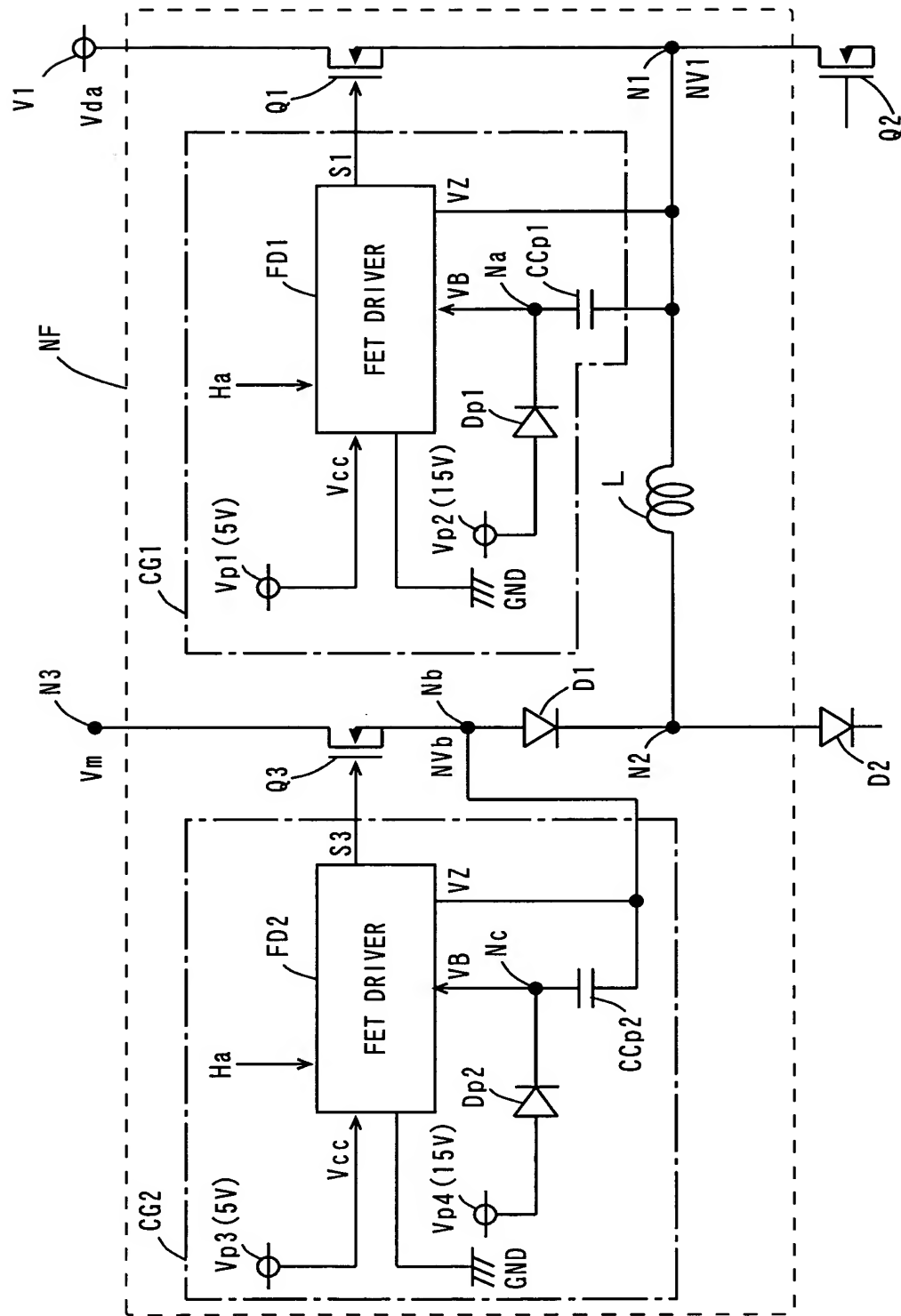
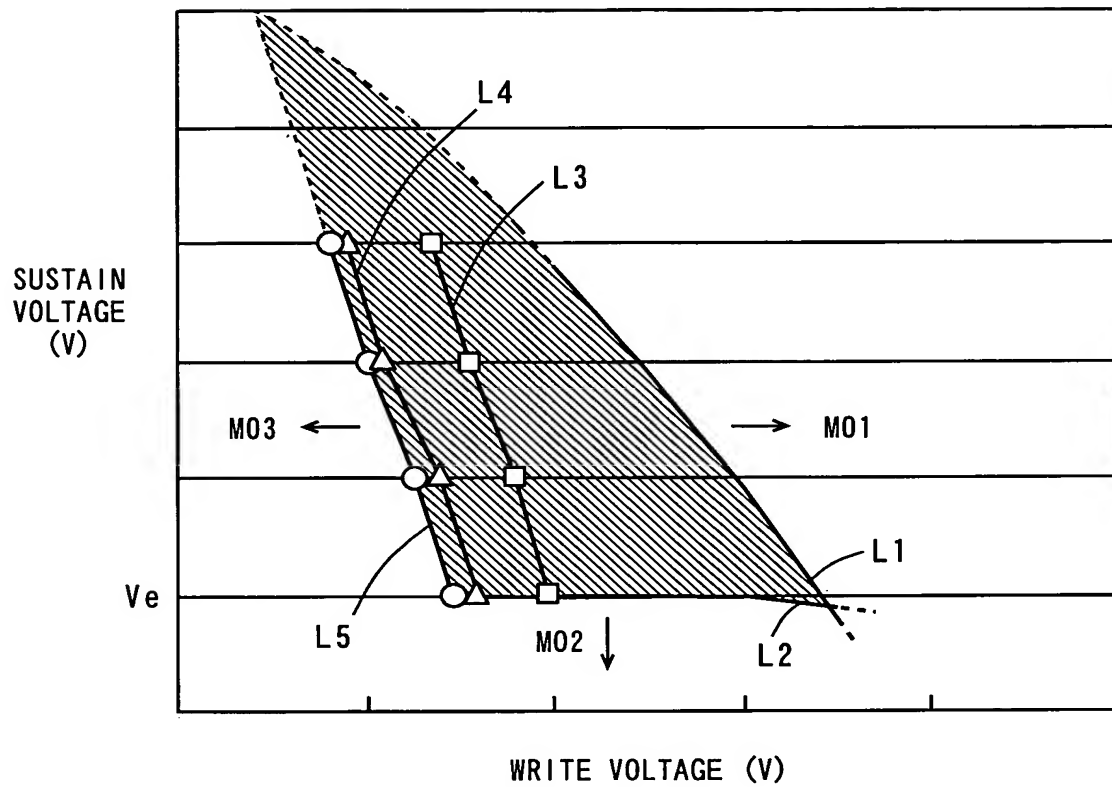


FIG. 17

DRIVING MARGIN (CASE OF $V_r=0.8V_{da}$)



- : PHASE DIFFERENCE 0
- △— : PHASE DIFFERENCE 150nsec
- : PHASE DIFFERENCE 200nsec

FIG. 18

WRITE VOLTAGE AT WHICH STABLE DISCHARGES CAN BE OBTAINED
(CASE WHERE SUSTAIN VOLTAGE IS TAKEN AS PREDETERMINED VOLTAGE
VALUE V_e , AND $V_r=0.8V_d$)

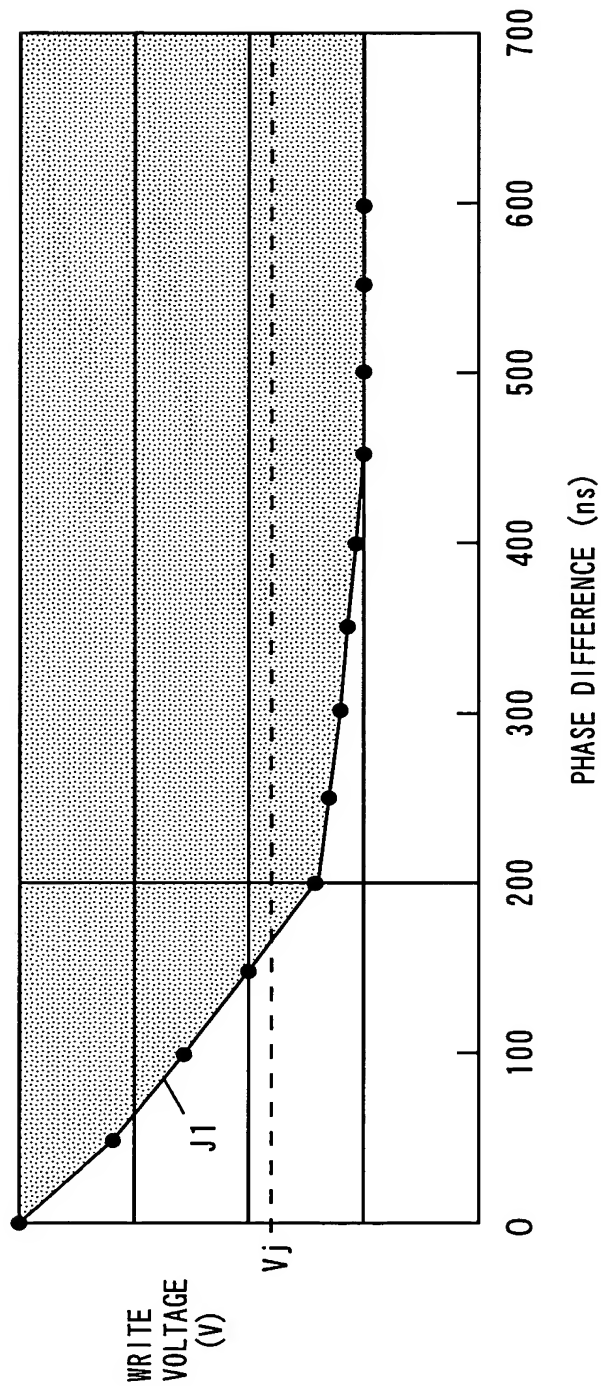


FIG. 19

WRITE VOLTAGE AT WHICH STABLE DISCHARGES CAN BE OBTAINED
(CASE WHERE SUSTAIN VOLTAGE IS TAKEN AS PREDETERMINED VOLTAGE
VALUE V_e , AND PHASE DIFFERENCE IS TAKEN AS 200ns)

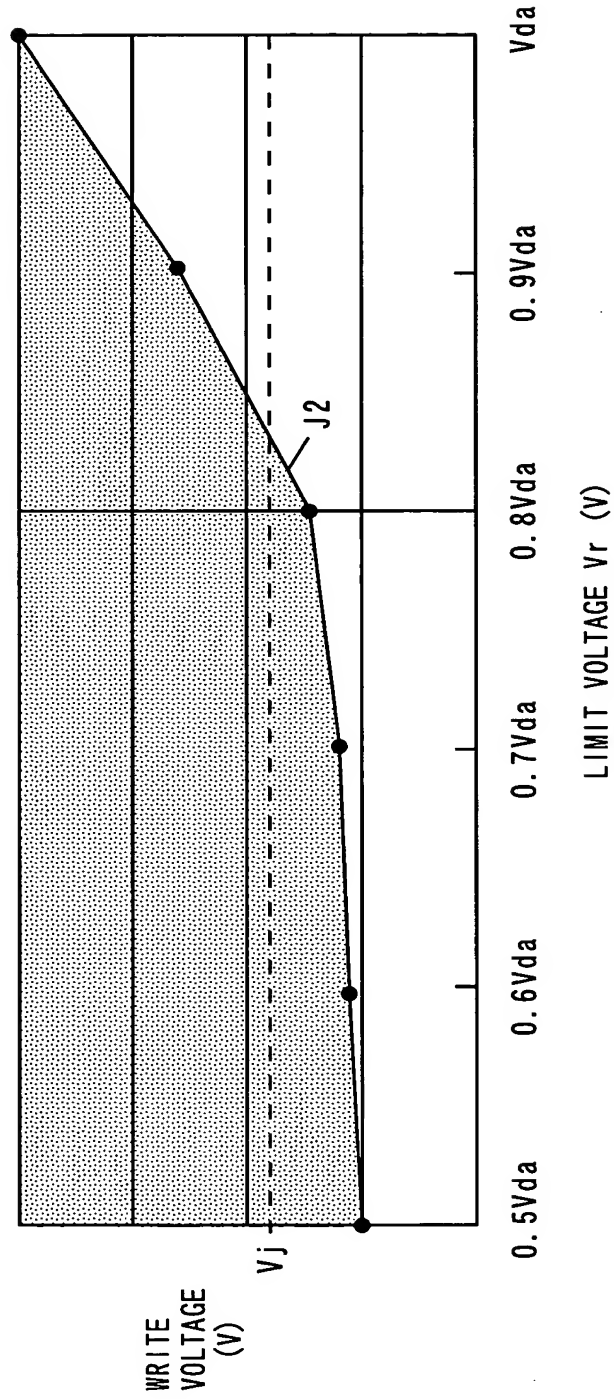


FIG. 20

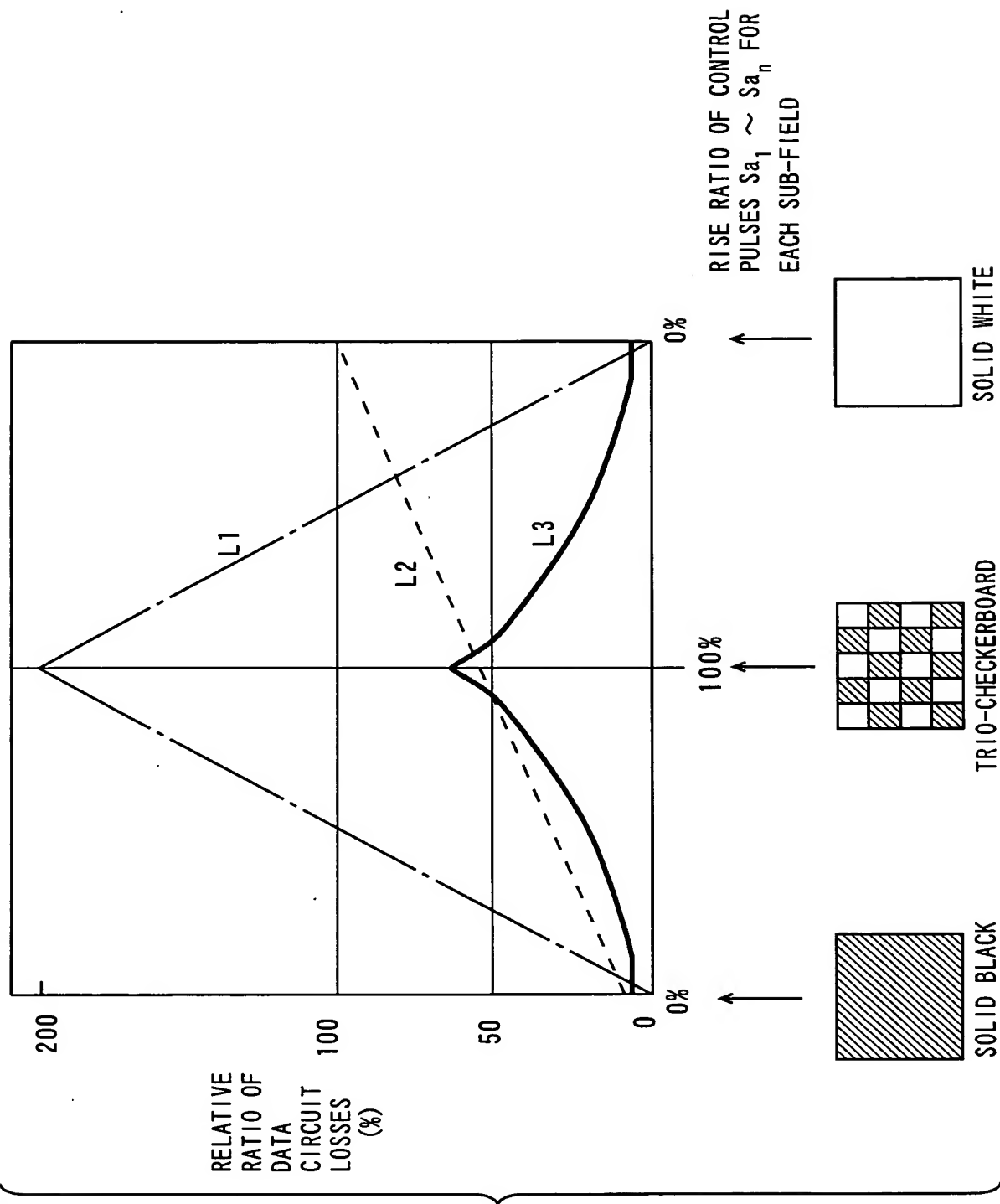
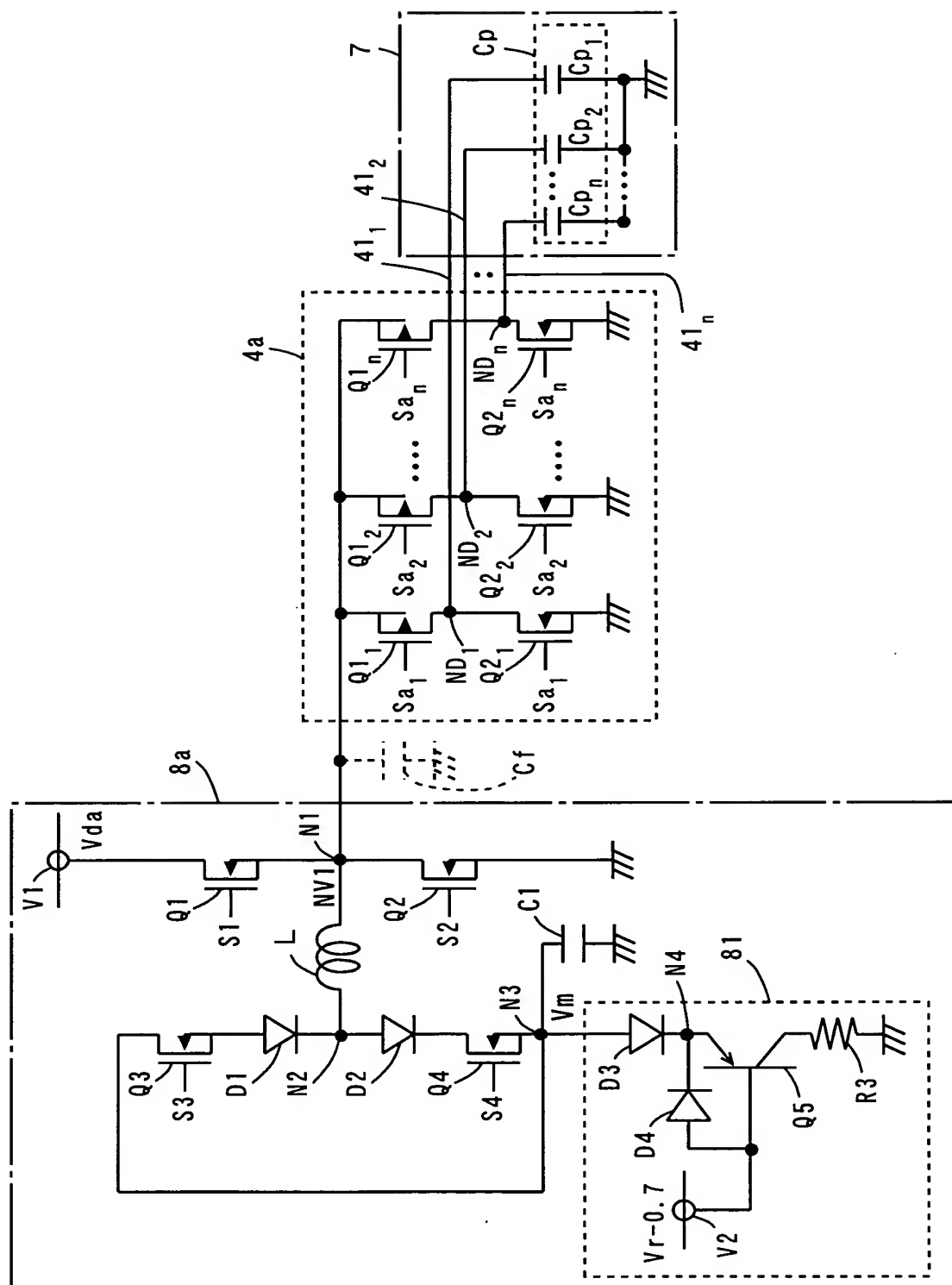


FIG. 21



F I G . 2 3

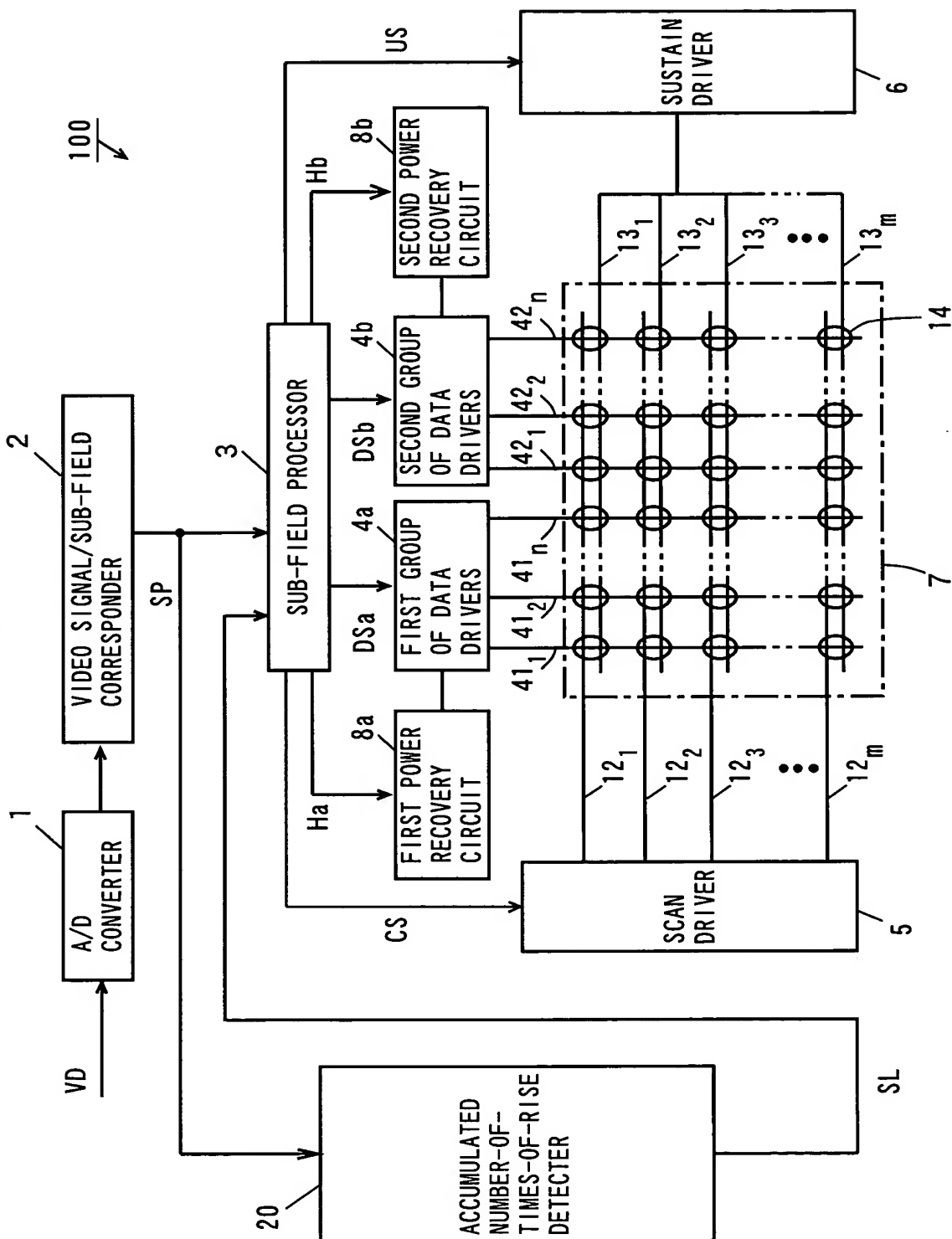


FIG. 24

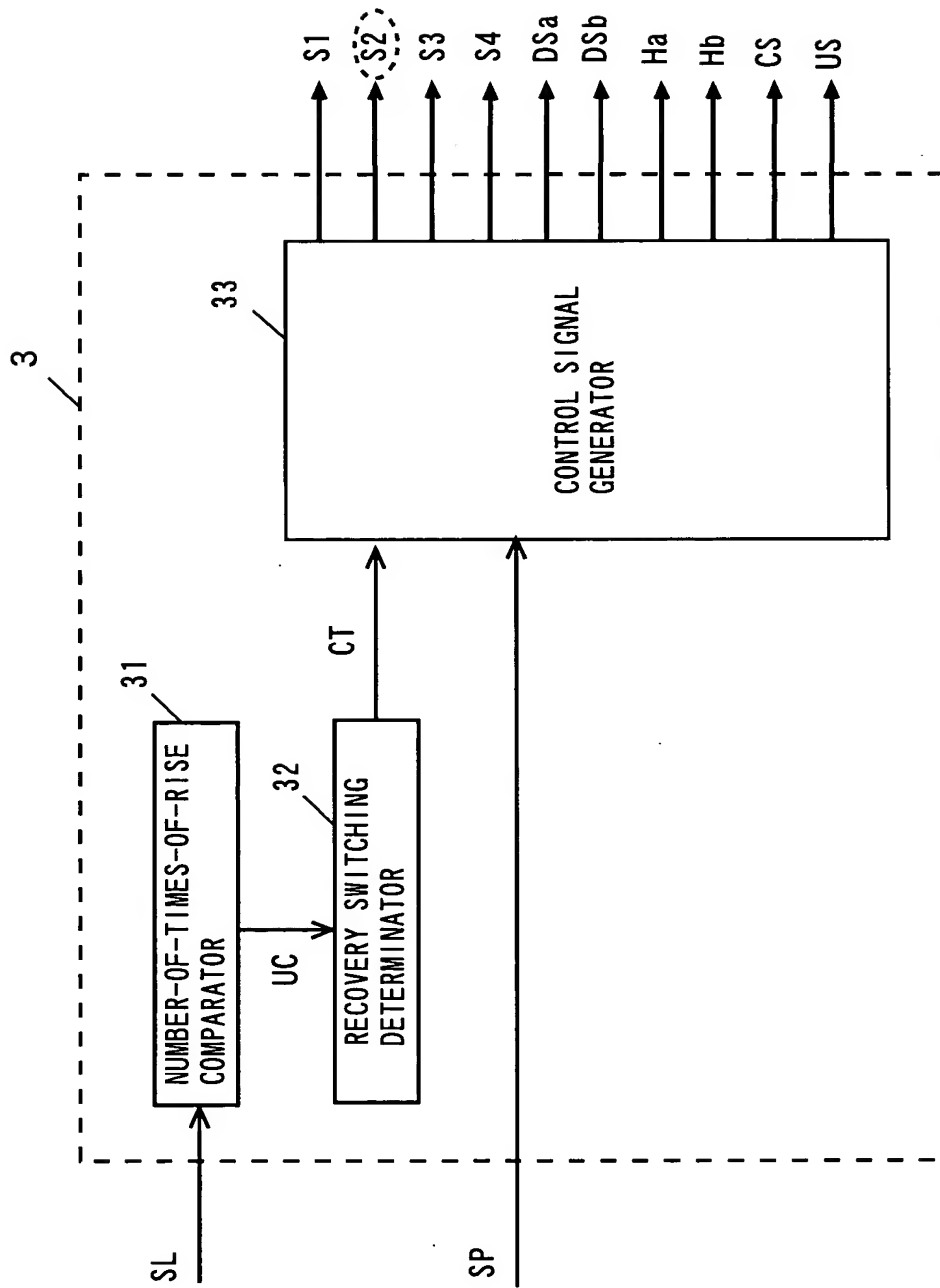


FIG. 25

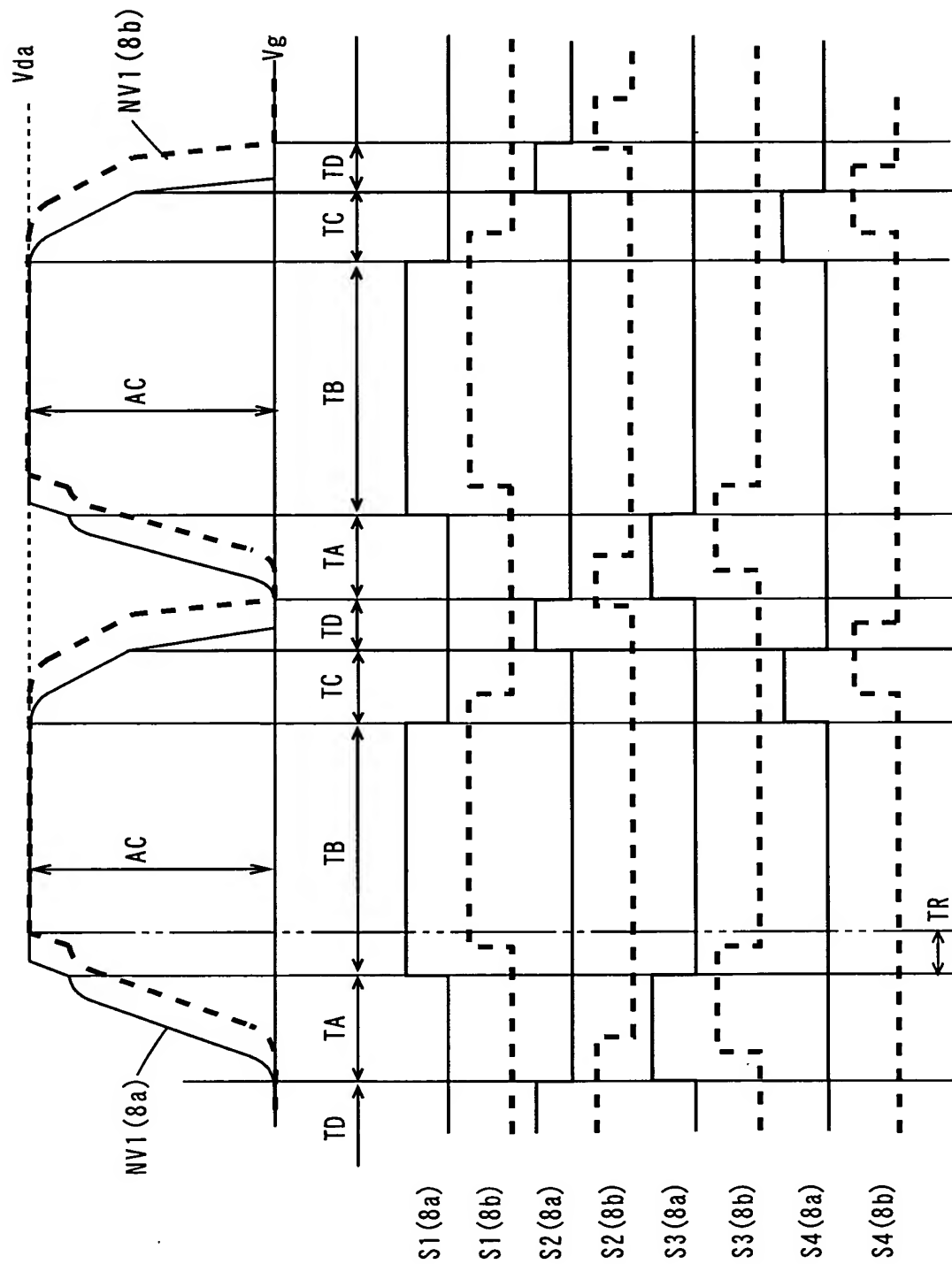


FIG. 26

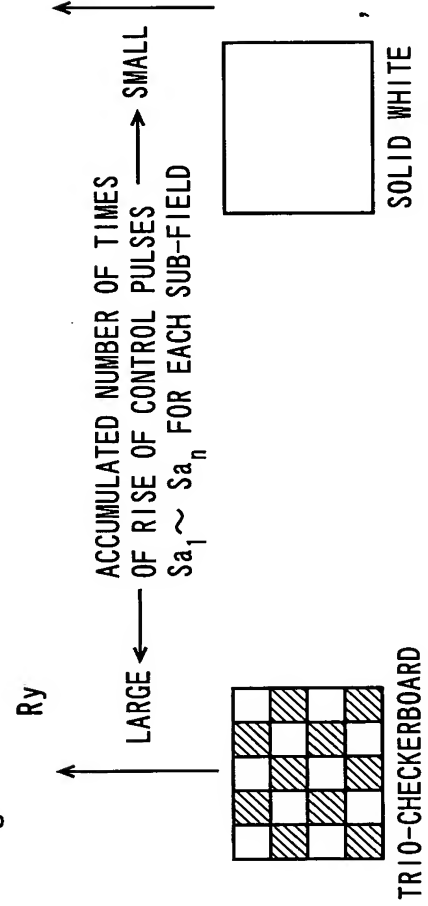
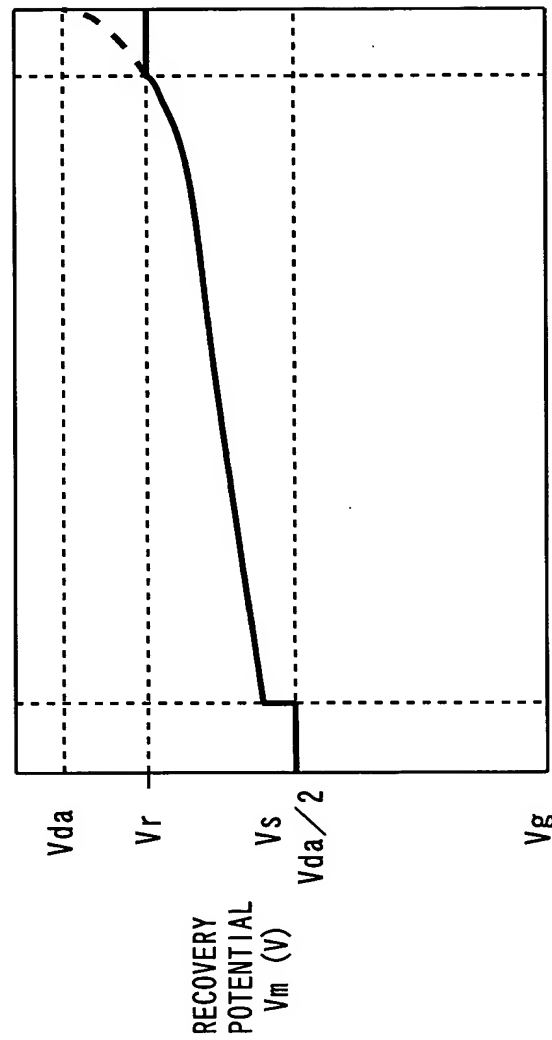


FIG. 27

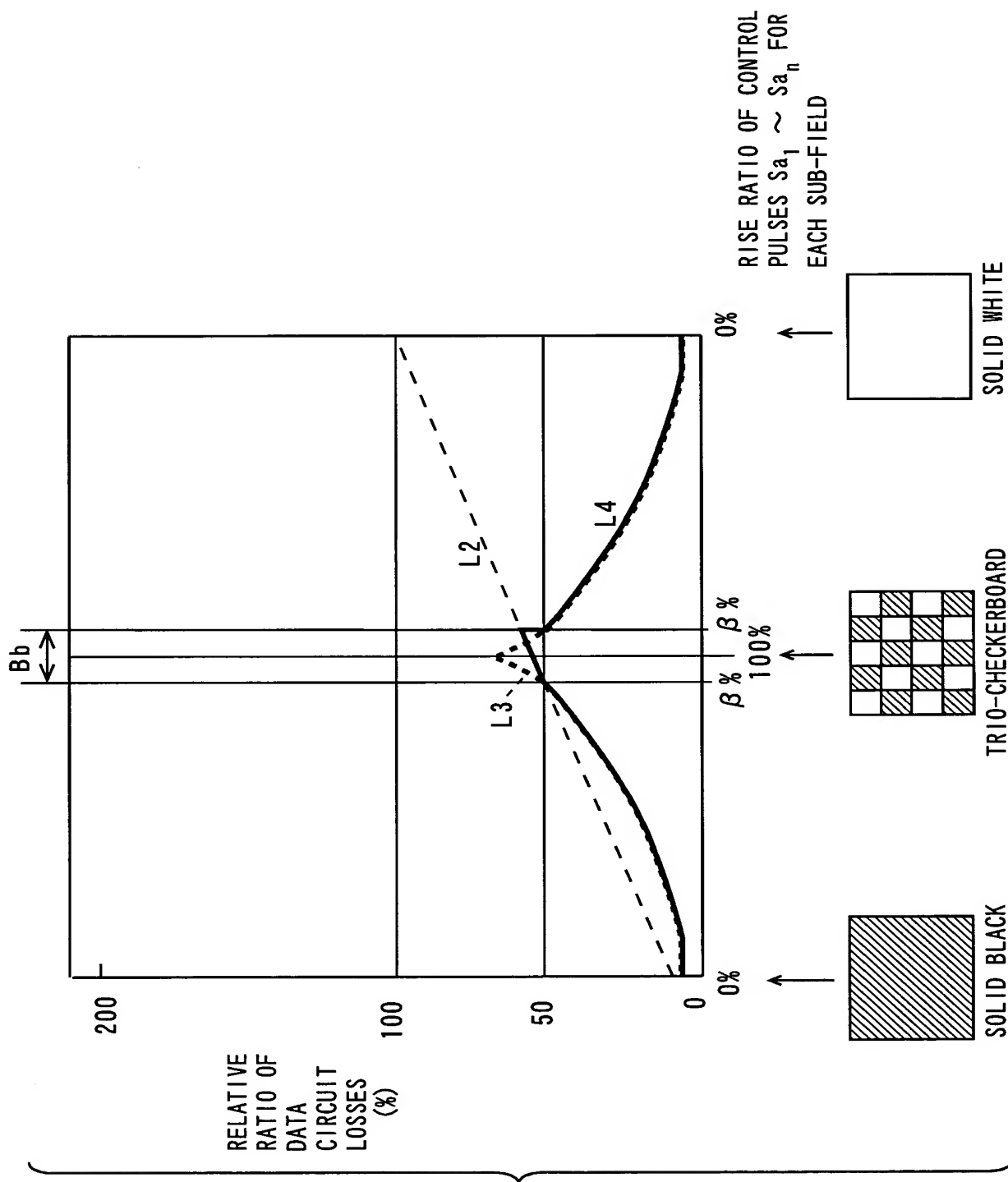


FIG. 28

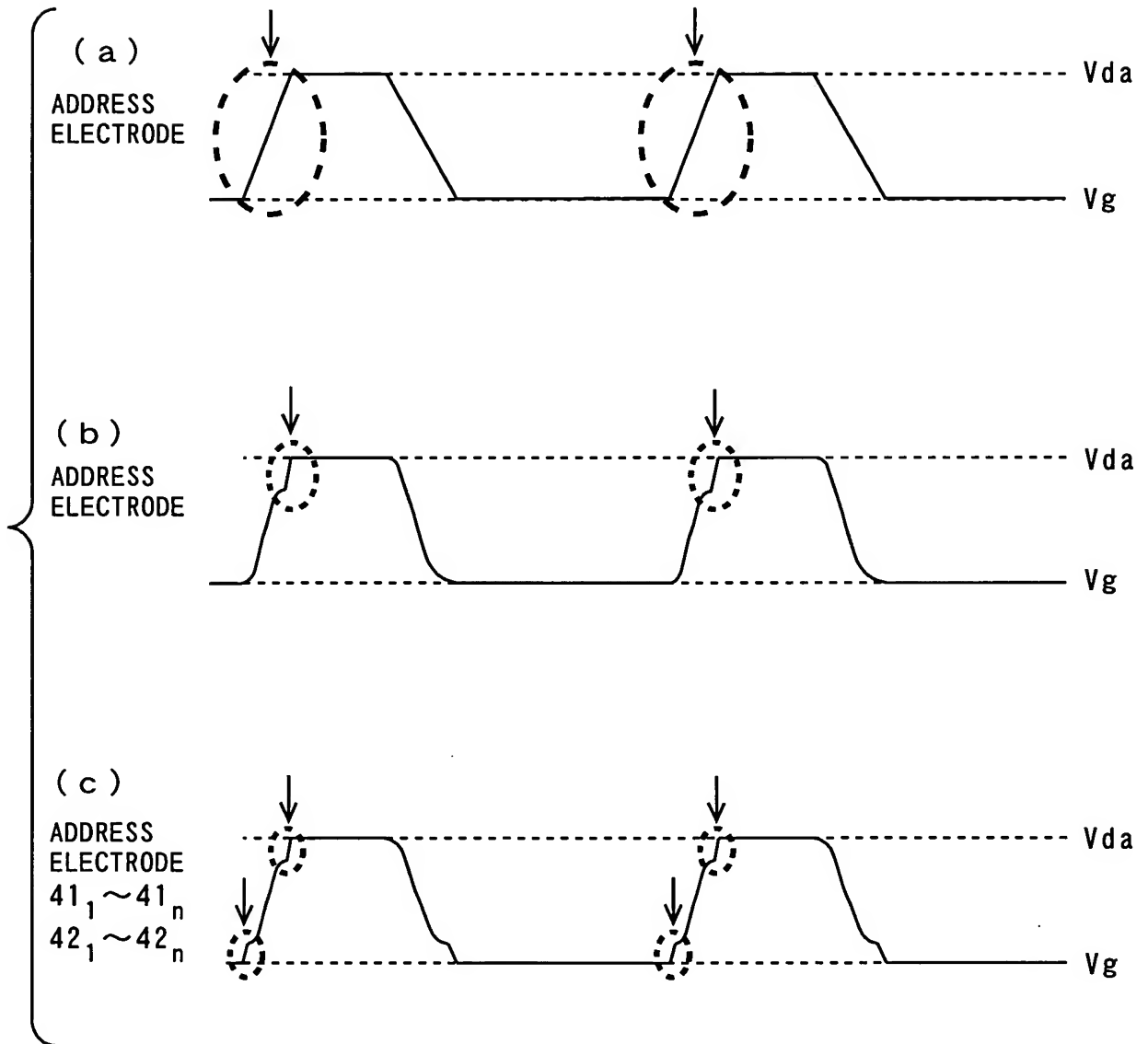


FIG. 29 PRIOR ART

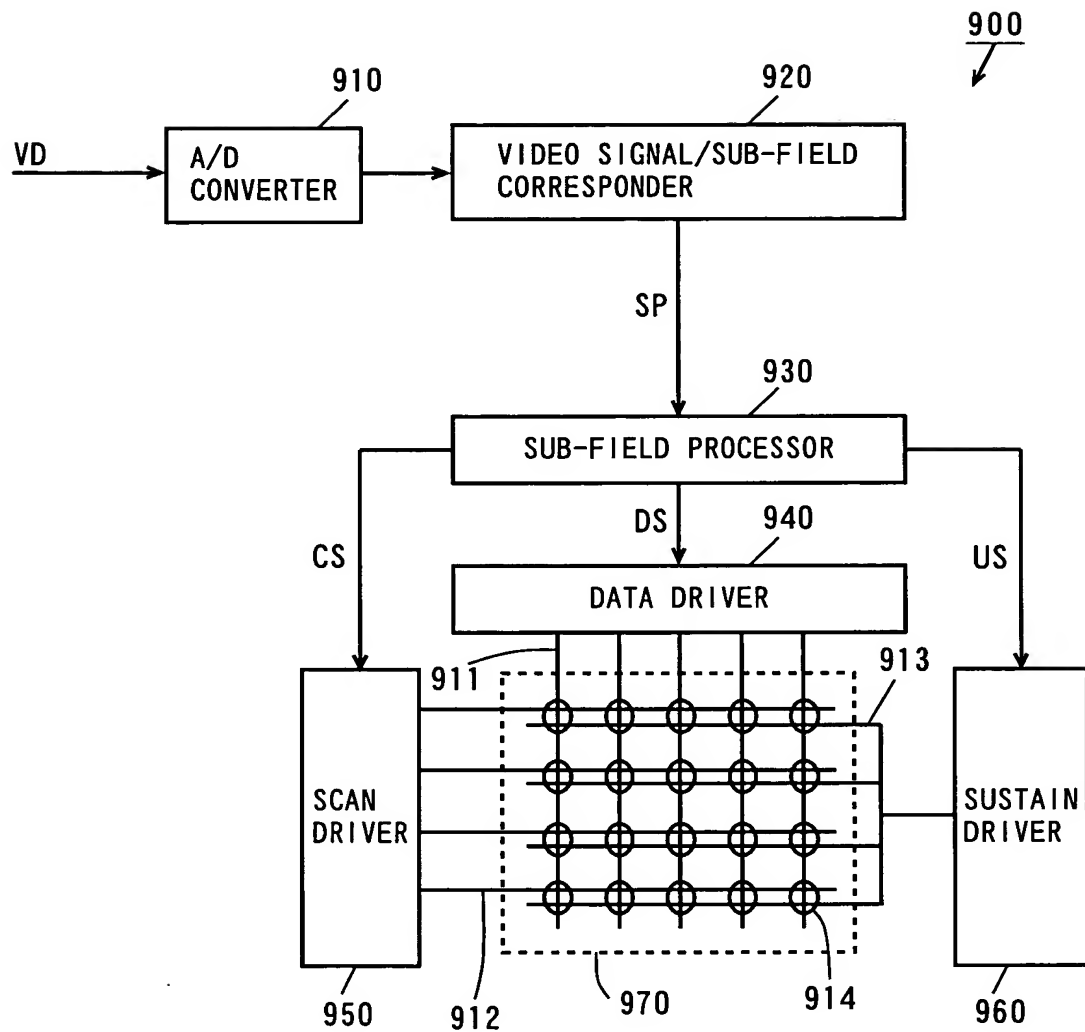


FIG. 30 PRIOR ART

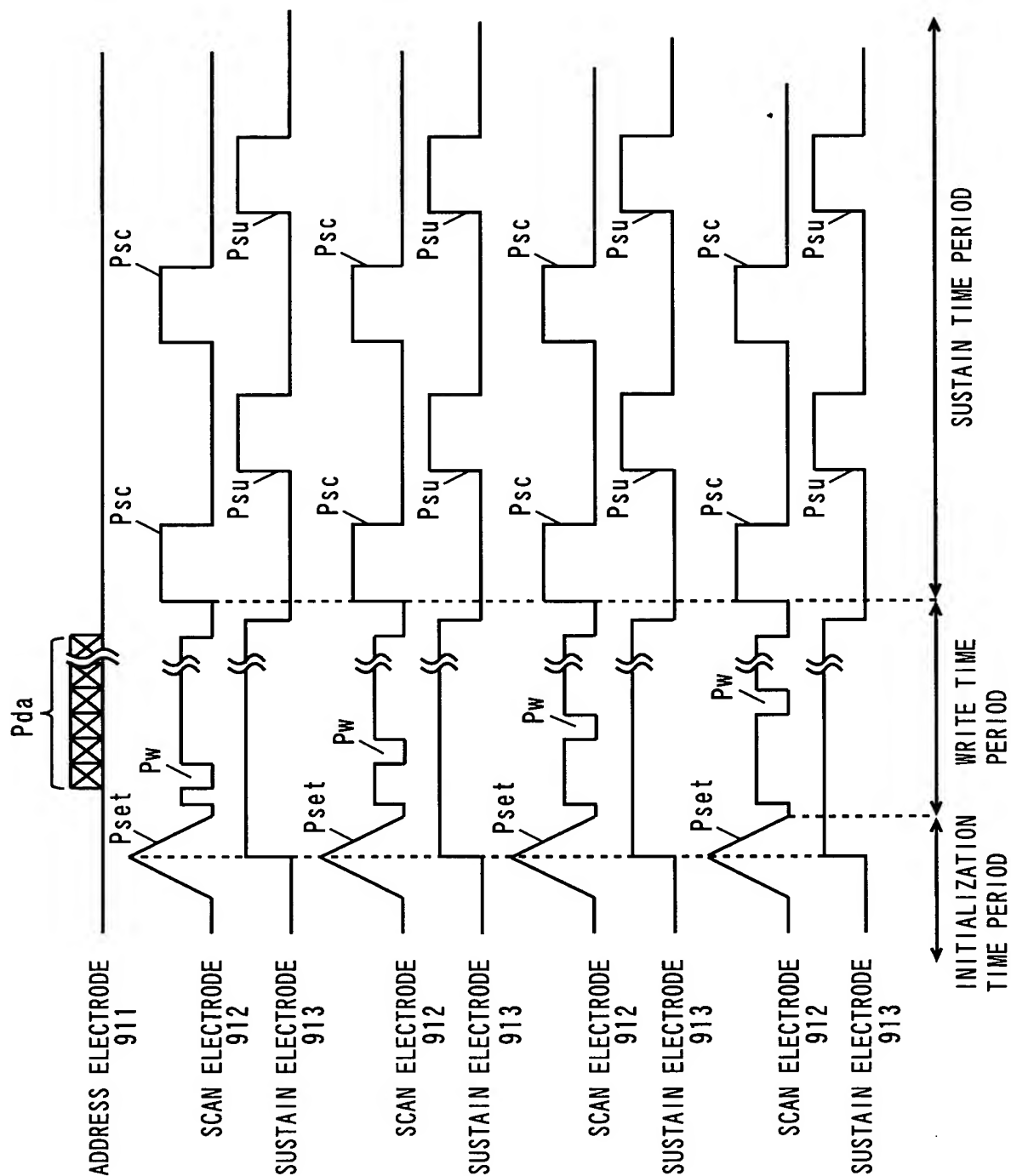


FIG. 31 PRIOR ART

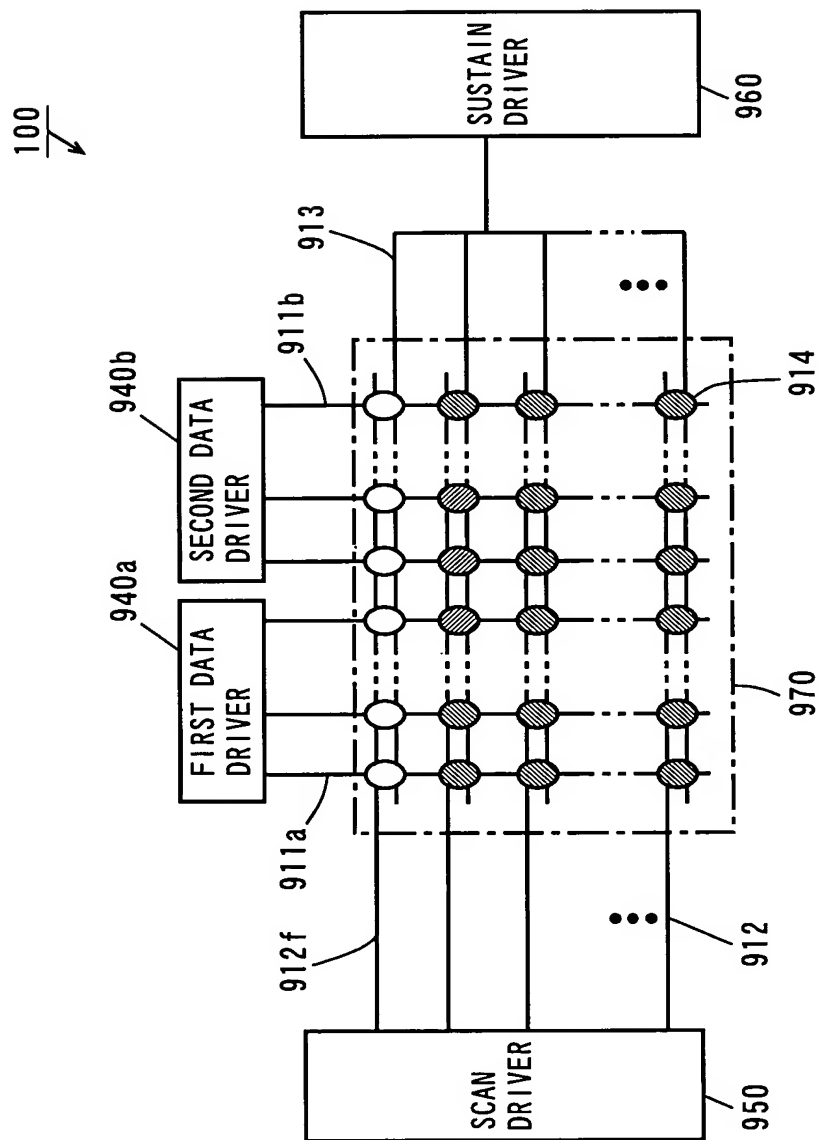


FIG. 32 PRIOR ART

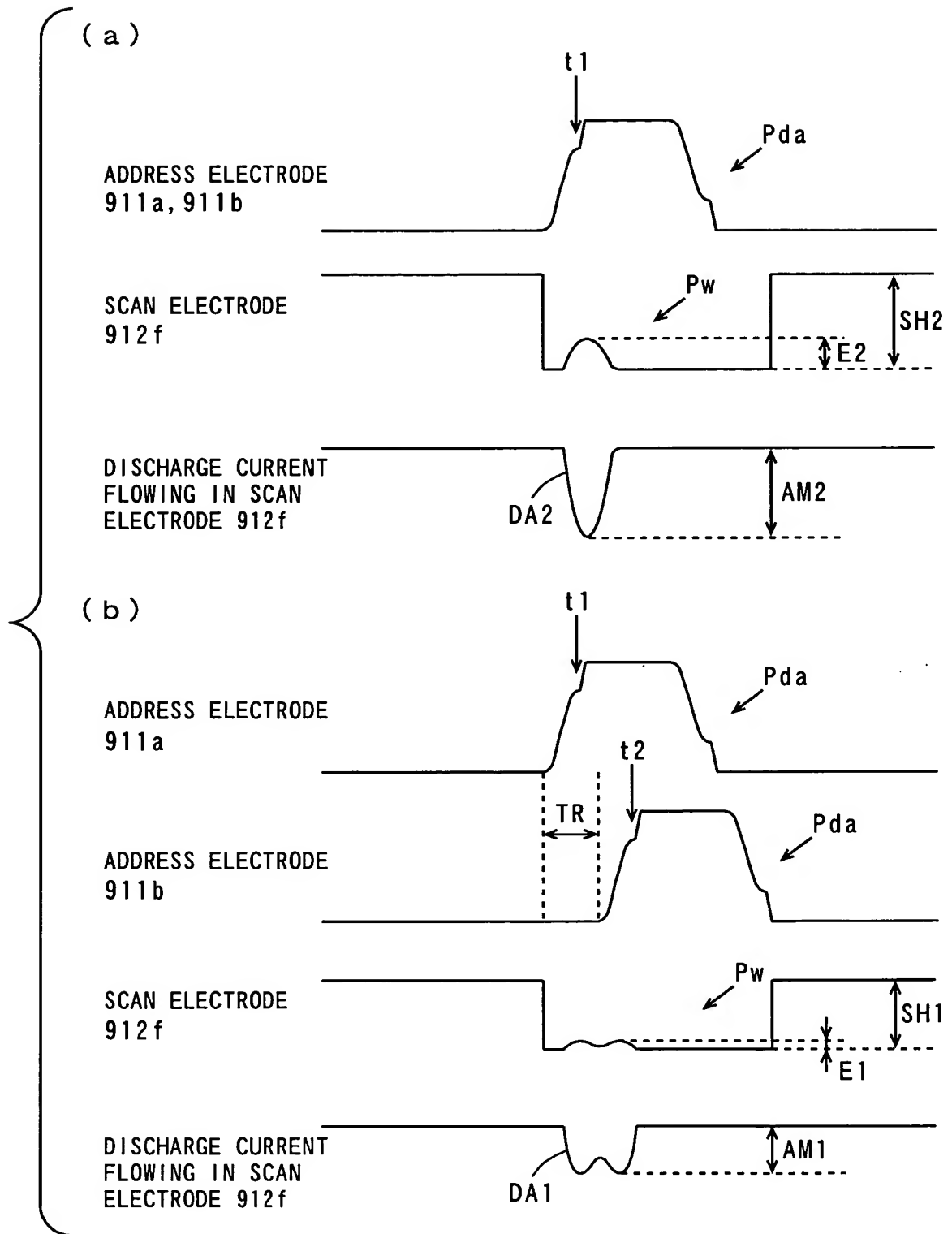


FIG. 33 PRIOR ART

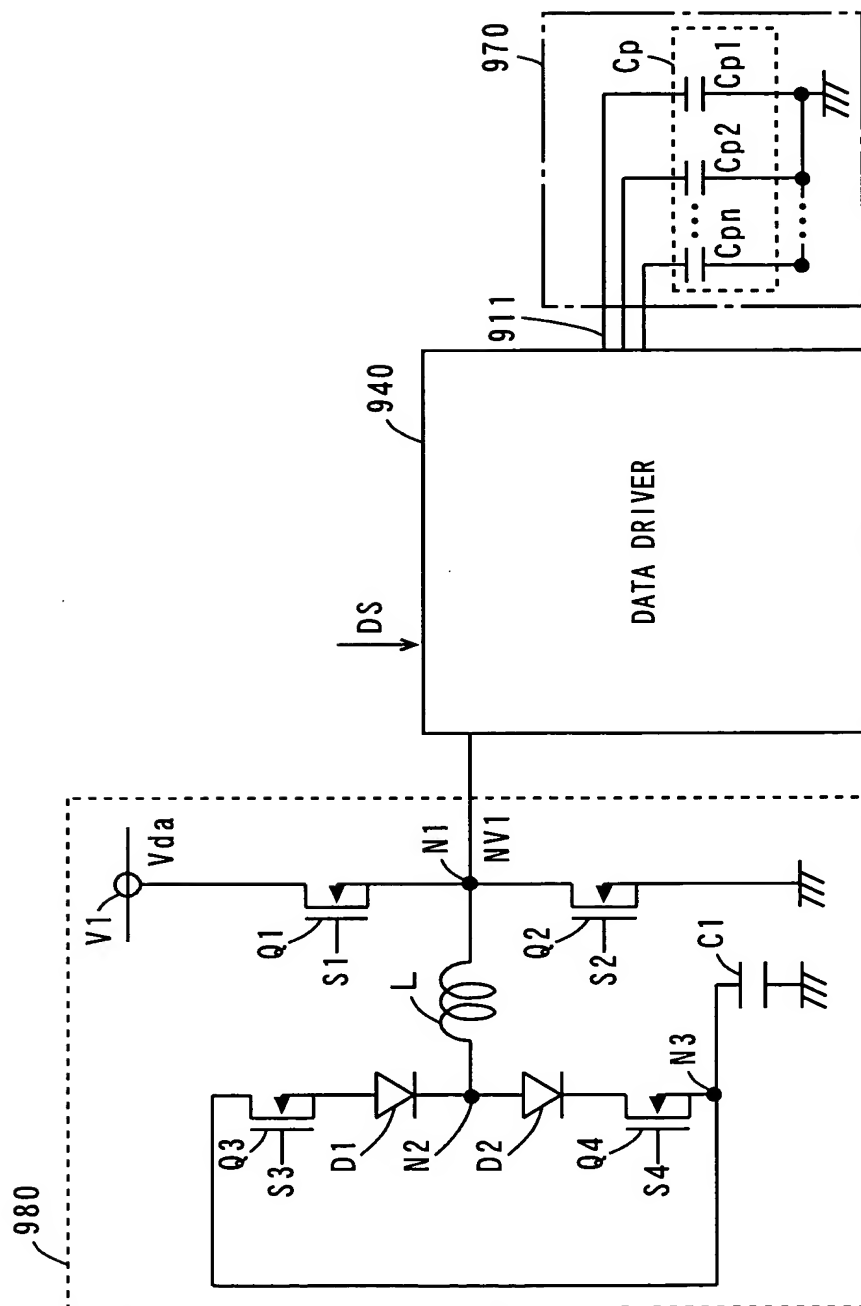


FIG. 34 PRIOR ART

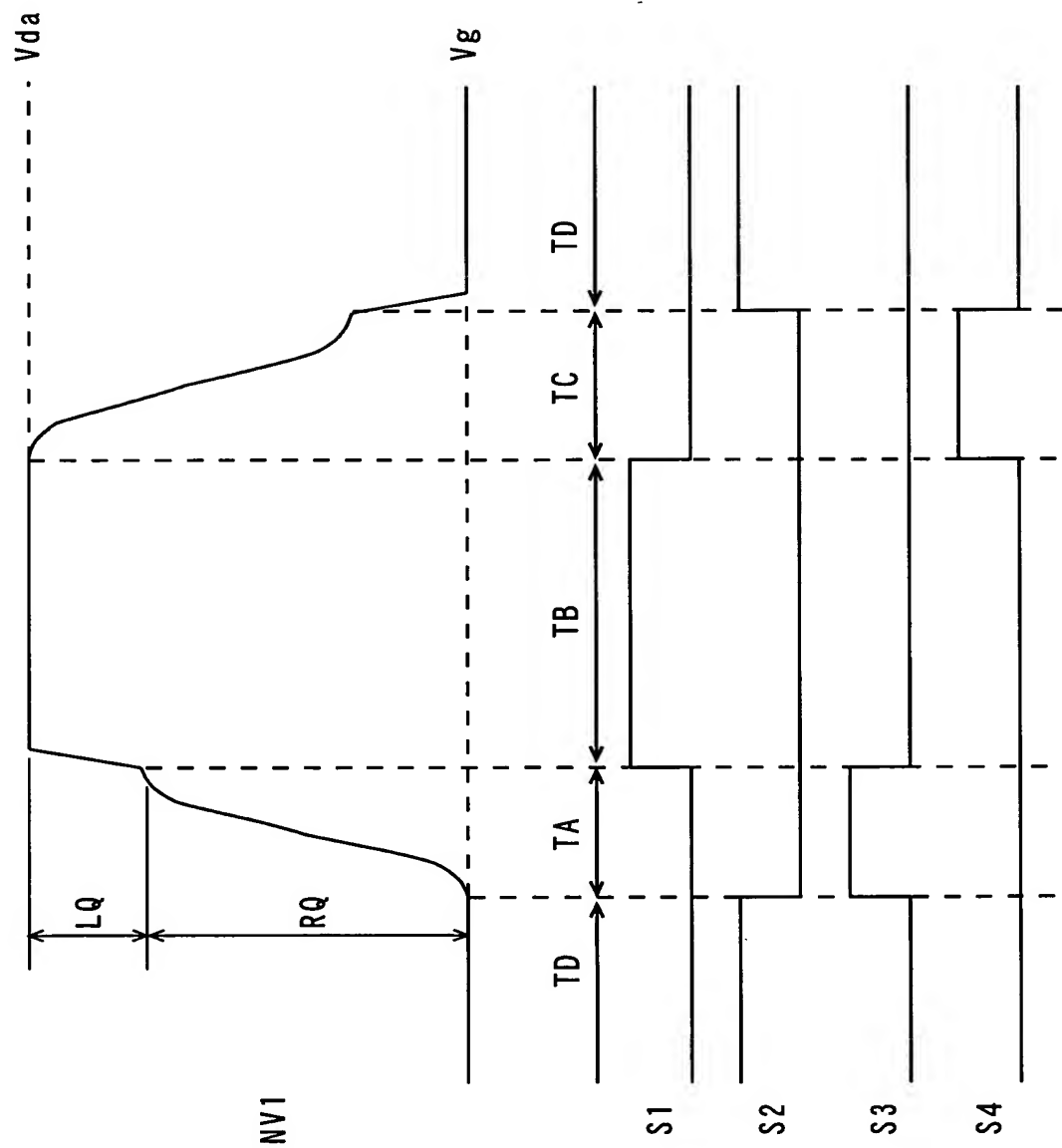


FIG. 35 PRIOR ART

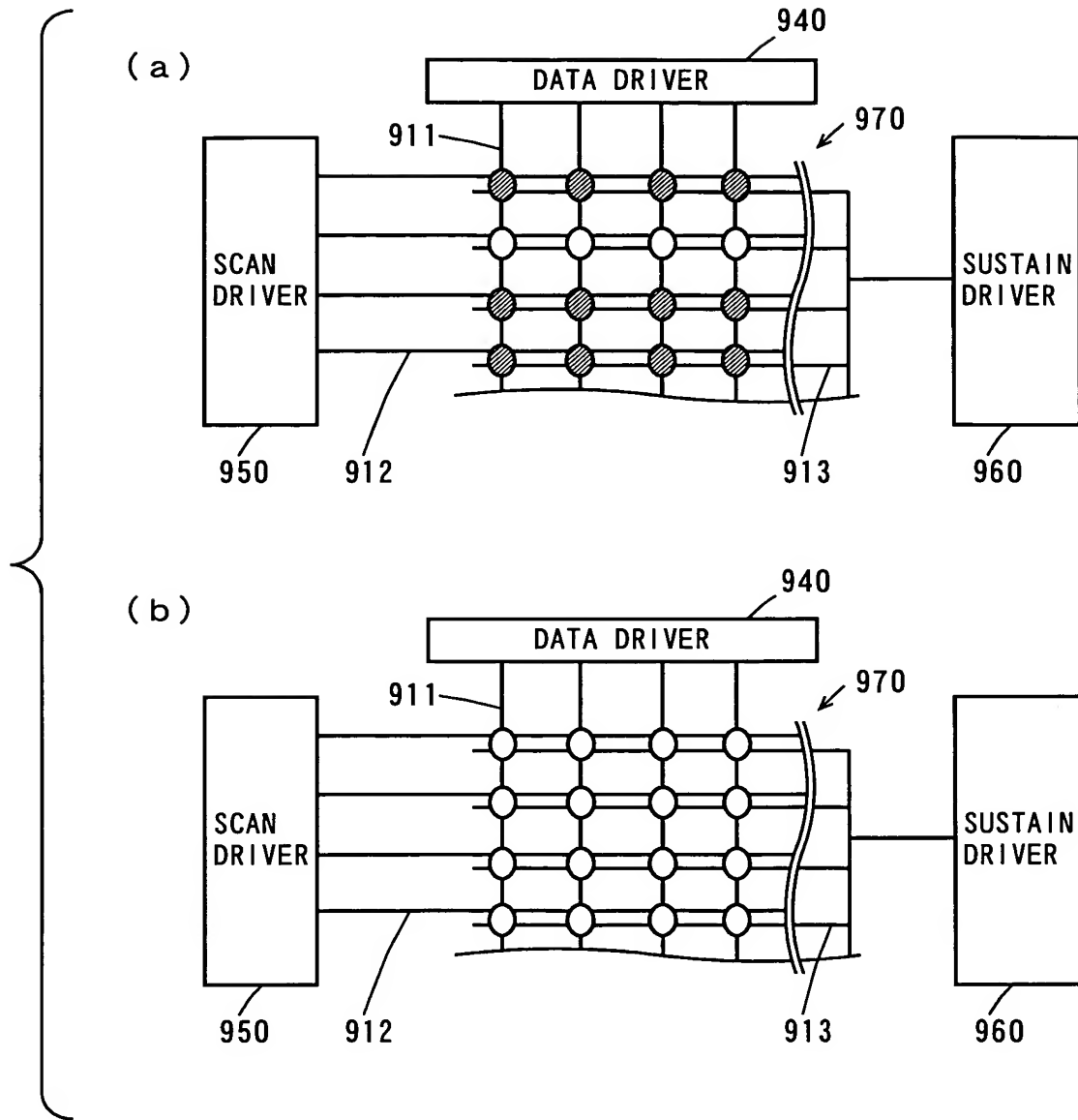


FIG. 36 PRIOR ART

